

Compal Confidential

EFL50/ EFT51 Schematics Document

Intel Dothan/ Celeron M/ Alviso GM(PM) / DDR-2 / ICH6-M

(Daughter Card: ATi M24P/ M26P)

2005 / 03 / 08 (B-Test EVT)

Rev:0.2

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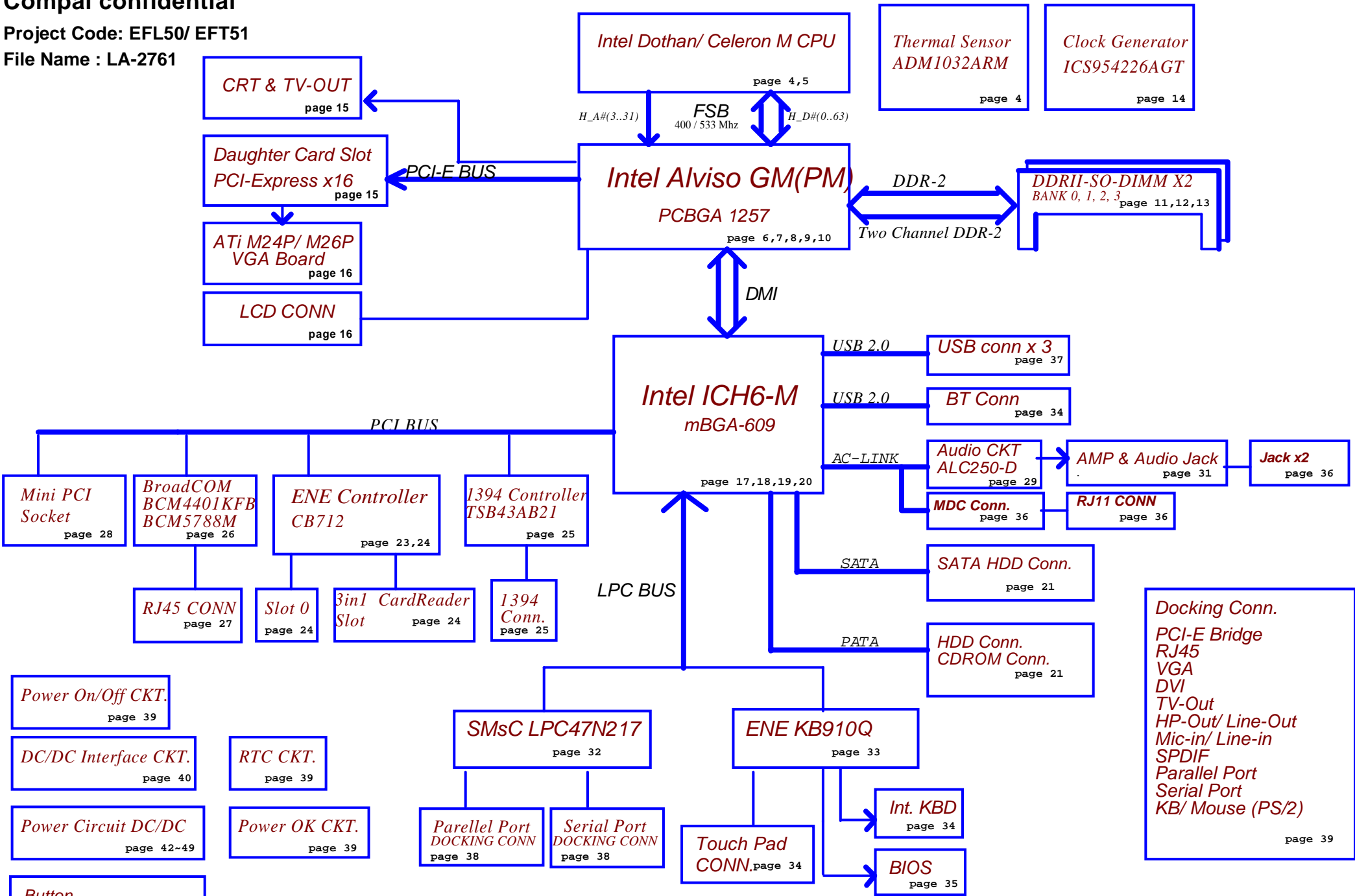
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P46-PWR-GMCH_CORE/1.8V/0.9V
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P50-PWR-PIR

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Issued Date	2005/03/08	Deciphered Date	2006/03/08	Cover Sheet	
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Project Code: EFL50/ EFT51

File Name : LA-2761



Security Classification		Compal Secret Data			
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				Block Diagrams	
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Voltage Rails

Power Plane	Description	S1	S3	S4/ S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+1.05VS	1.05V switched power rail	ON	OFF	OFF
+DDRVTT	0.9V switched power rail for DDR terminator	ON	OFF	OFF
+1.5VALW	1.5V always on power rail	ON	ON	ON*
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+DDRVCC	1.8V power rail for DDR	ON	ON	OFF
+2.5VS	2.5V switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3V	3.3V power rail	ON	ON	OFF
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+5VMOD	5V switched power rail for Module Bay	ON	OFF	OFF
+12VALW	12V always on power rail	ON	ON	ON*
+RTCVCC	RTC power	ON	ON	ON

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

External PCI Devices

Device	IDSEL#	REQ#/GNT#	Interrupts
CardBus	AD20	2	PIRQA/PIRQB
1394	AD16	0	PIRQE
SD	AD20	2	PIRQA/PIRQB
Mini-PCI	AD18	1	PIRQG/PIRQH
LAN	AD17	3	PIRQF

EC SM Bus1 address

Device	Address	Device	Address
Smart Battery	0001 011X b	ADM1032	1001 110X b
EEPROM(24C16/02)	1010 000X b		
(24C04)	1011 000Xb		

ICH6M SM Bus address

Device	Address
Clock Generator (ICS 954226AGT)	1101 001Xb
DDR# DIMM0	1001 000Xb
DDR# DIMM2	1001 010Xb

STATE \ SIGNAL	SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1(Power On Suspend)	LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

Board ID / SKU ID Table for AD channel

Vcc	3.3V +/- 5%			
Ra / Rc	100K +/- 5%			
Board ID	Rb / Rd	VAD_BID min	VAD_BID typ	VAD_BID max
0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

BOARD ID Table

Board ID	PCB Revision
0	0.1
1	0.2
2	
3	
4	
5	
6	
7	

SKU ID Table

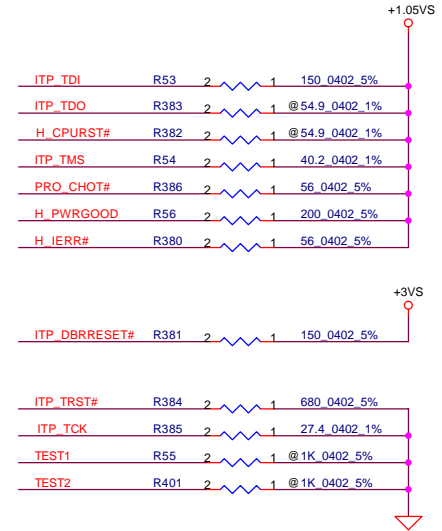
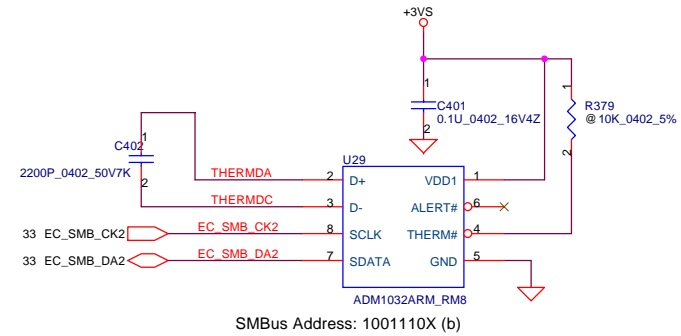
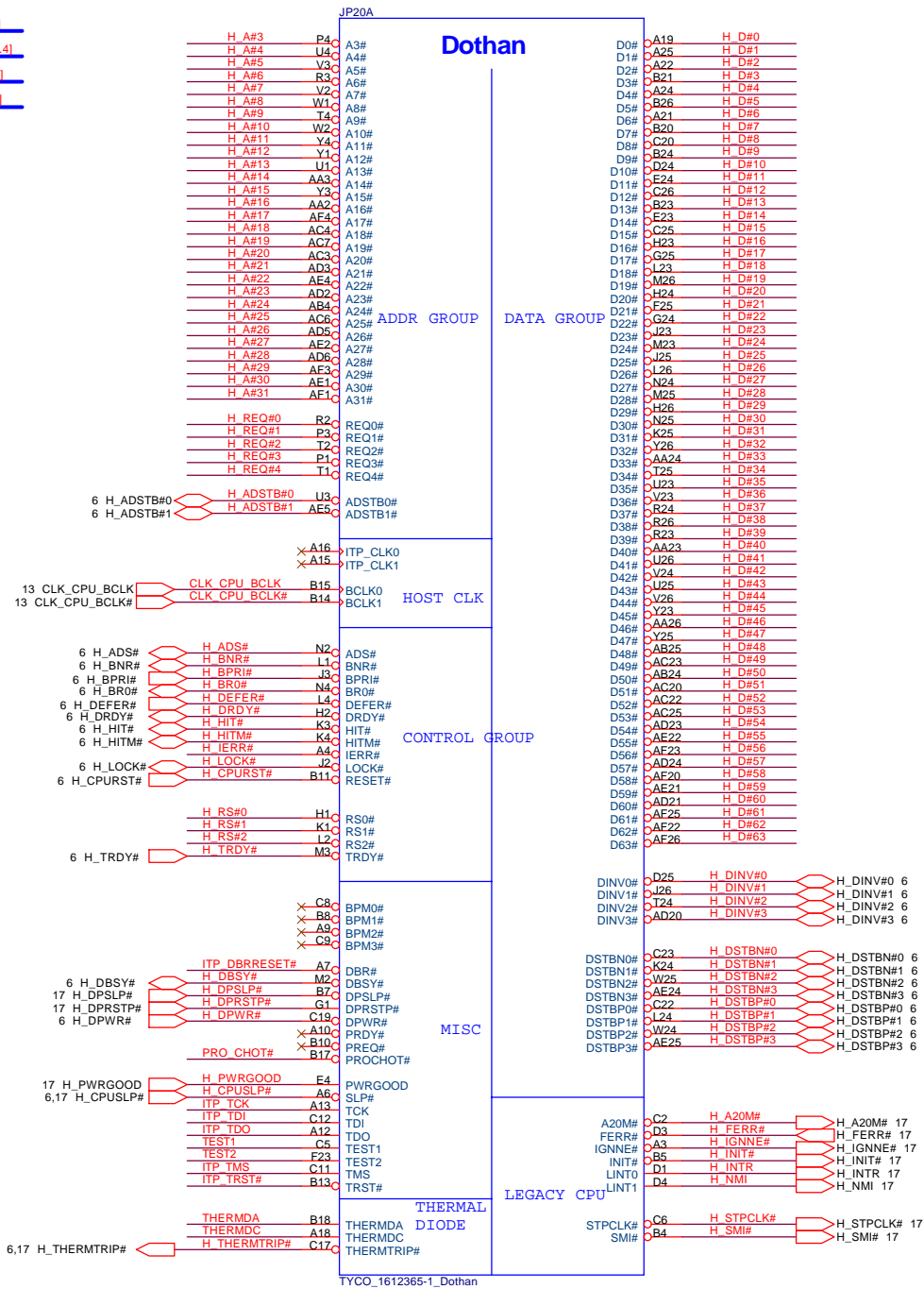
SKU ID	SKU
0	
1	
2	
3	
4	
5	
6	
7	

BTO Option Table

BTO Item	BOM Structure
UMA	GM@
Discrete	PM@
LAN 10/100	4401@
LAN GIGA	5788@
1 Spindle	1S@
2 Spindle	2S@
2 Spindle with SATA	2SS@
2 Spindle with PATA	2SP@
1 Spindle with SATA	1SS@
1 Spindle with PATA	1SP@
With Docking	WD@
Without Docking	ND@
With 1394	1394@
With 1394 4pin	1394<4>@
With 1394 6pin	1394<6>@

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C		I		D		E		F			
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6 H_A#[3..31] H_A#[3..31]
6 H_REQ#[0..4] H_REQ#[0..4]
6 H_RS#[0..2] H_RS#[0..2]
6 H_D#[0..63] H_D#[0..63]

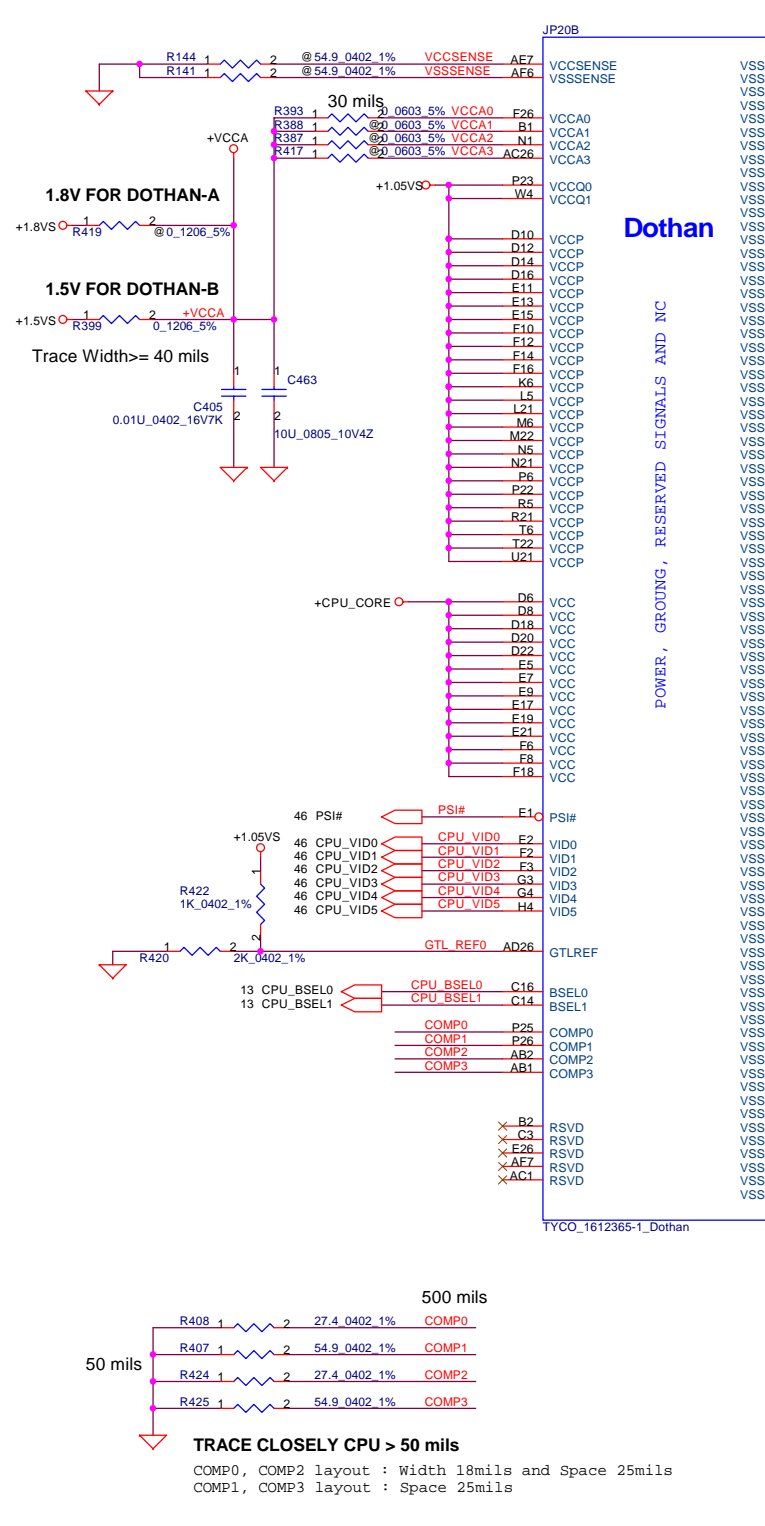


0415

H_INIT# C685 1 2 47P_0402_50V8J
H_NMI C686 1 2 47P_0402_50V8J
H_SMI# C687 1 2 47P_0402_50V8J
H_CPURST# C688 1 2 47P_0402_50V8J

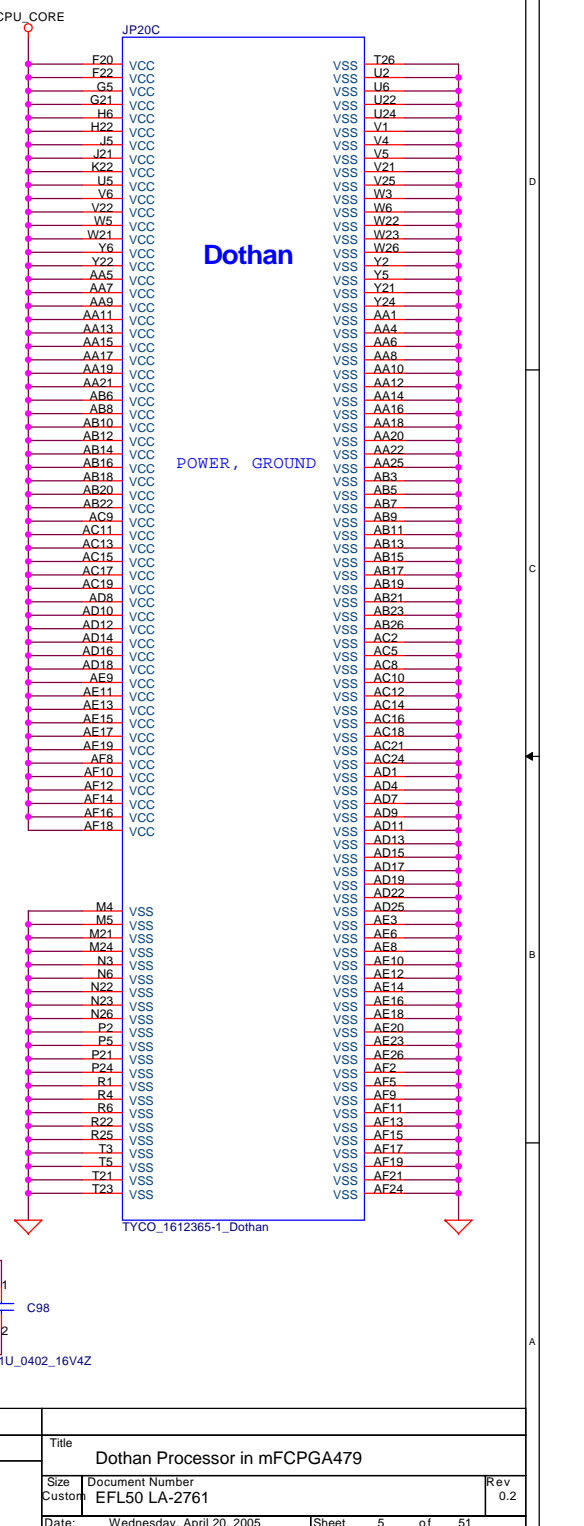
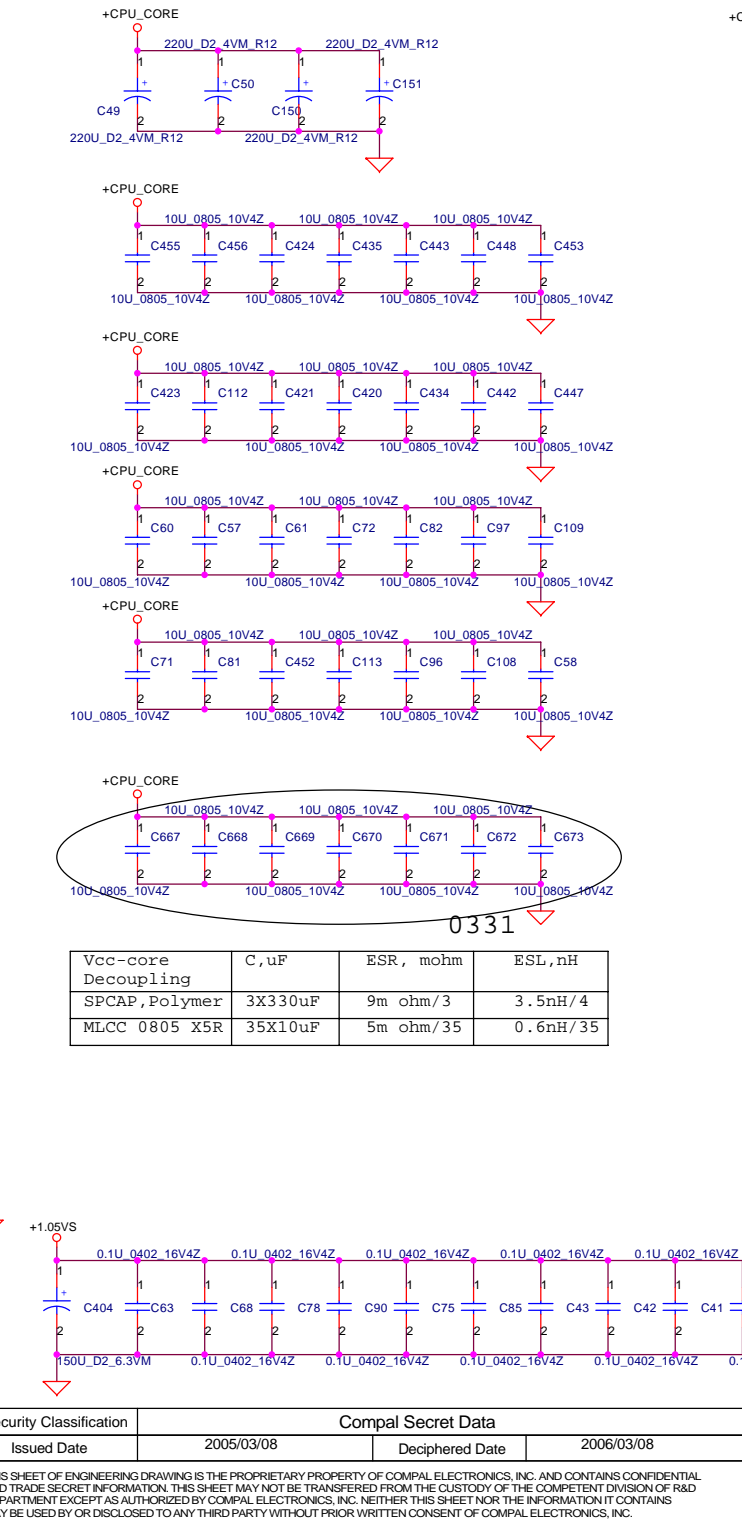
THERMDA & THERMDC Trace / Space = 10 / 10 mil

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Issued Date	2005/03/08	Deciphered Date	2006/03/08	Dothan Processor in mFCPGA479	
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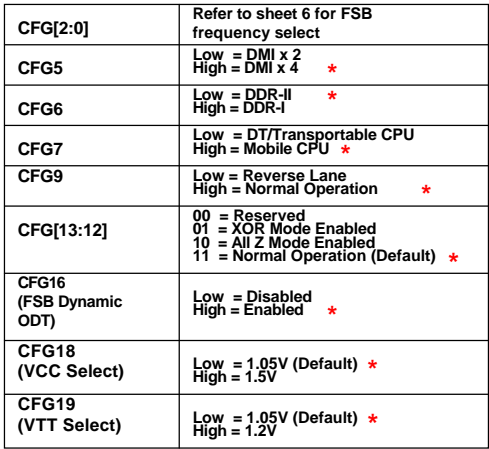


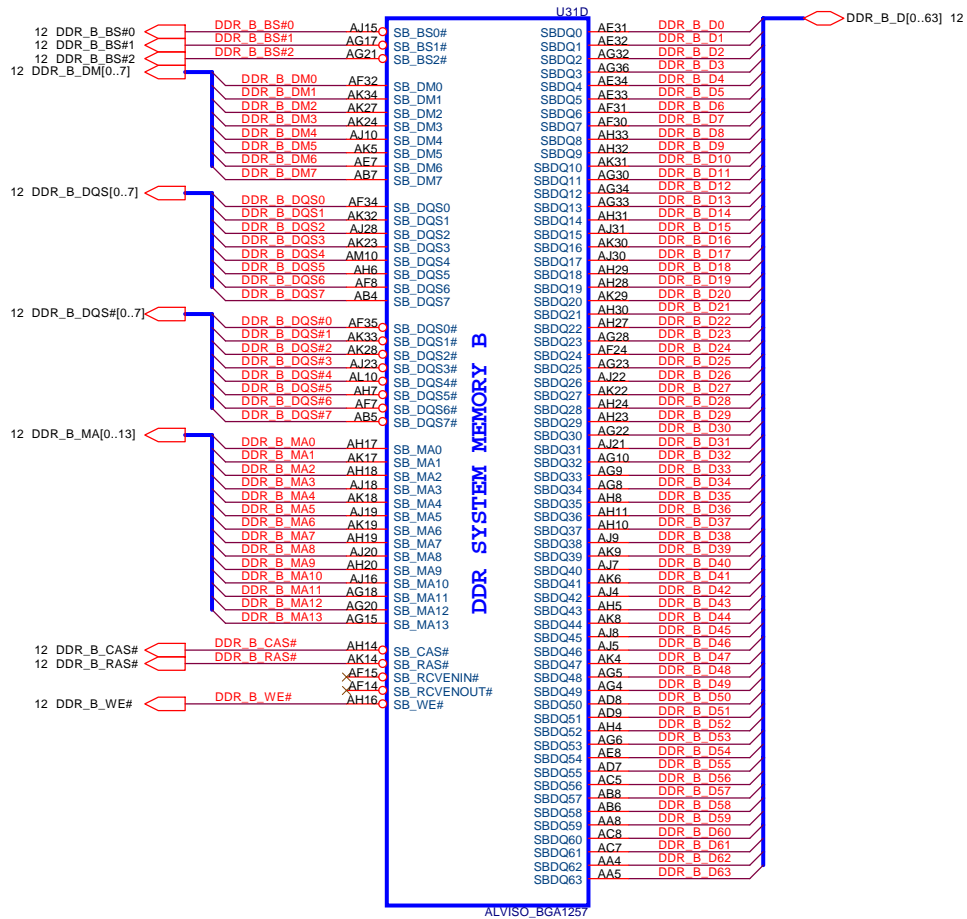
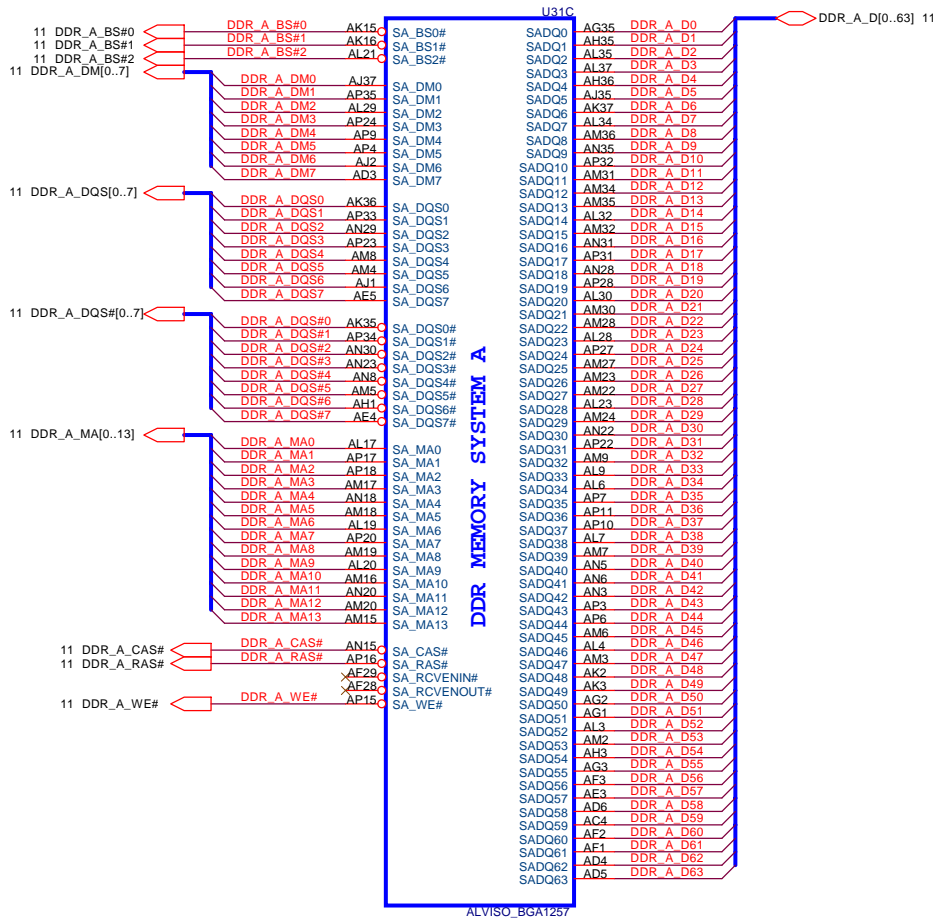
Dothan

POWER, GROUND, RESERVED SIGNALS AND NC

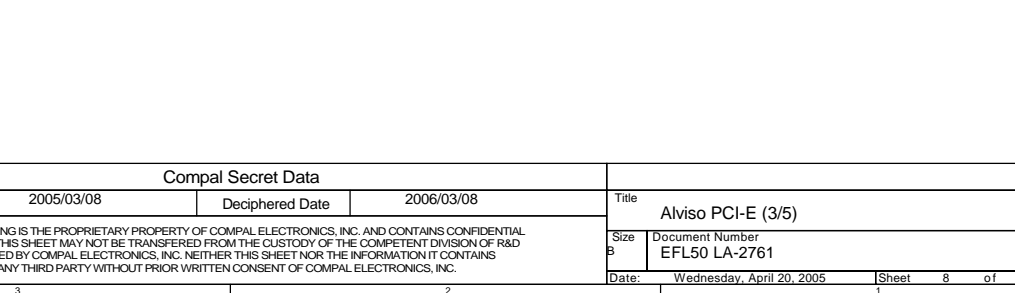
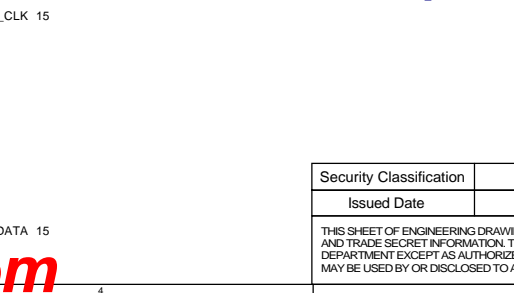
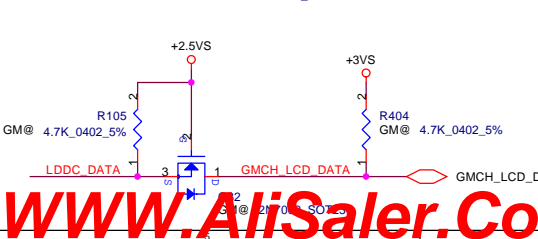


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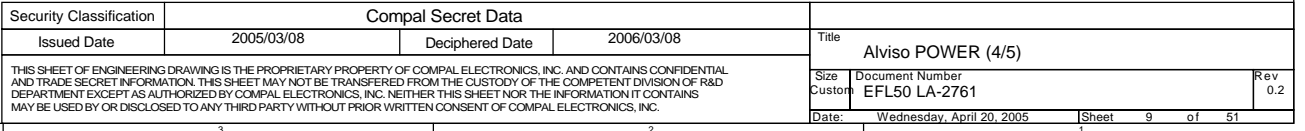


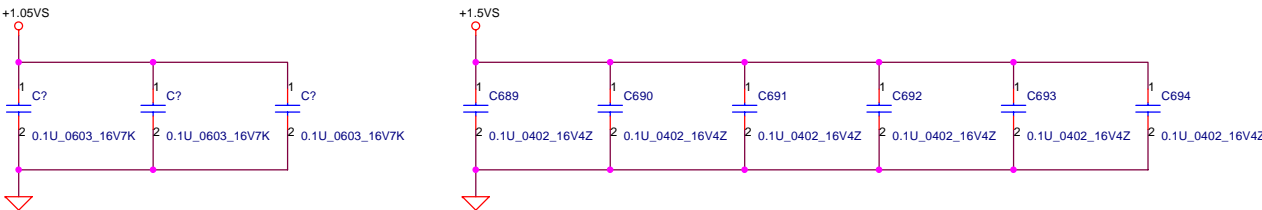
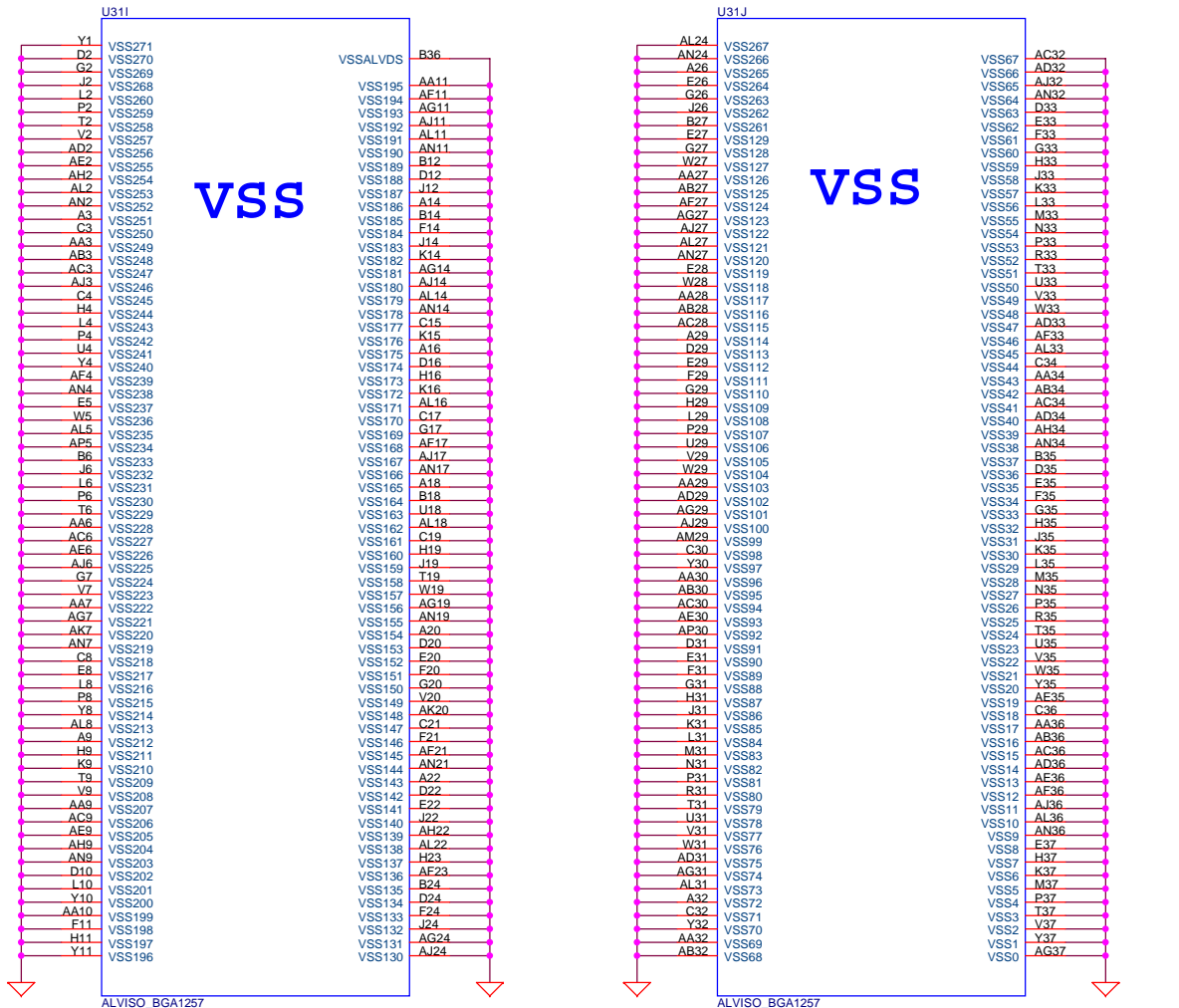
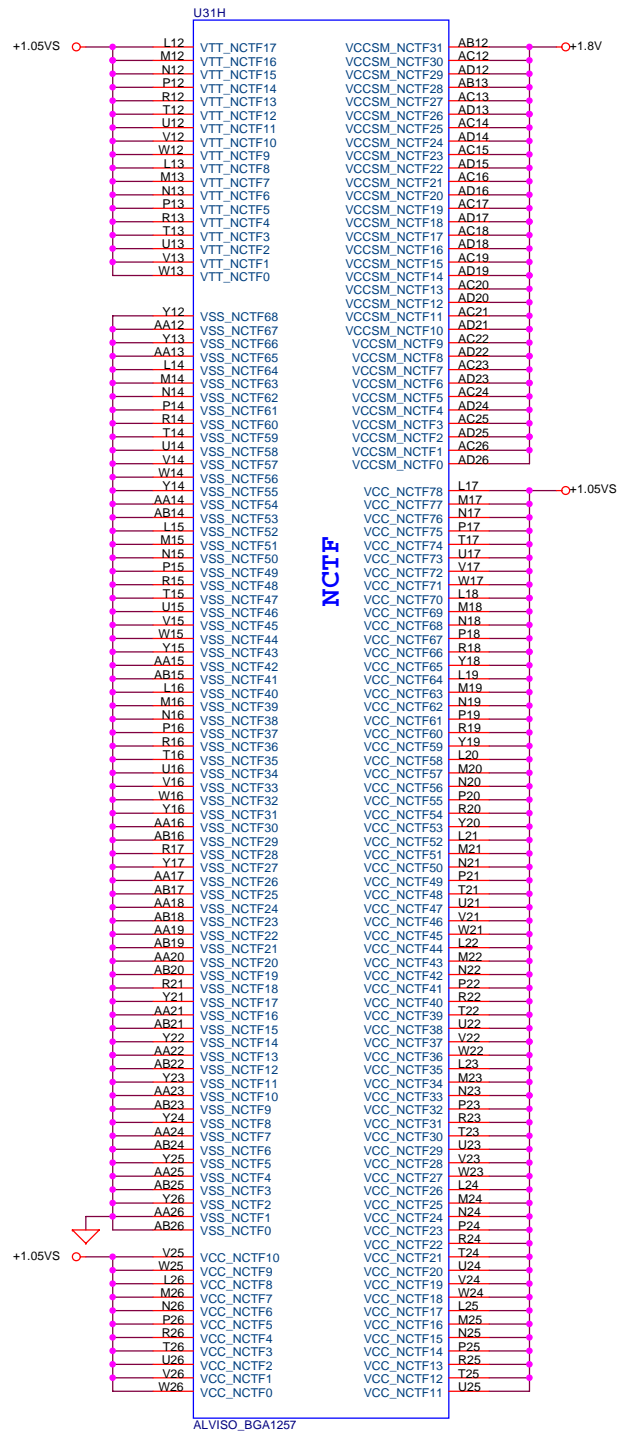


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Issued Date	2005/03/08	Deciphered Date	2006/03/08	Title Alviso DDR (2/5)		
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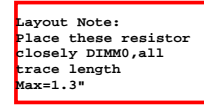


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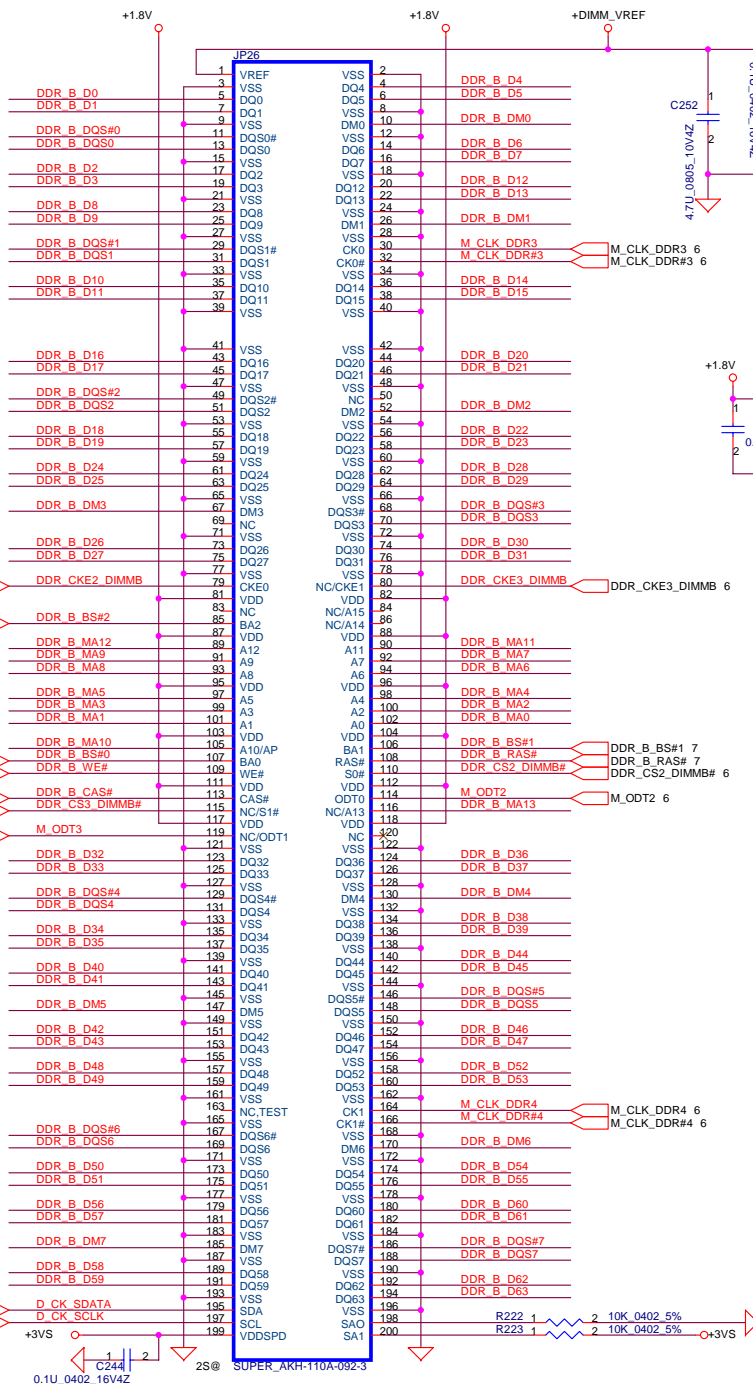




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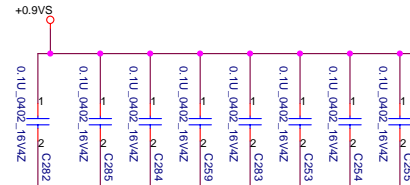
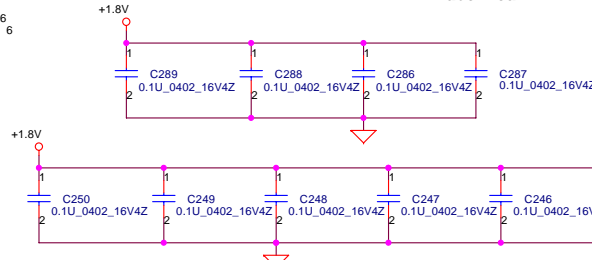
Security Classification		Compal Secret Data		Title DDRII- SODIMM SLOT0	
Issued Date	2005/03/08	Deciphered Date	2006/03/08		
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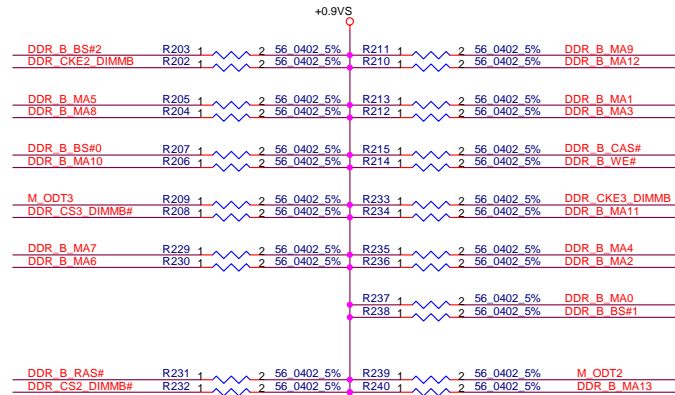
Address: 1001 010X b

7 DDR_B_D[0..63] **DDR B D[0..63]**
 7 DDR_B_DM[0..7] **DDR B DM[0..7]**
 7 DDR_B_DQS[0..7] **DDR B DQS[0..7]**
 7 DDR_B_MA[0..13] **DDR B MA[0..13]**
 7 DDR_B_DQS#[0..7] **DDR B DQS#[0..7]**

Layout Note:
Place near DIMM



Layout Note:
Place one cap close to every 2 pullup resistors terminated to +0.9VS



Layout Note:
Place these resistor closely DIMM0, all trace length < 750 mil

Layout Note:
Place these resistor closely DIMM0, all trace length Max=1.3"

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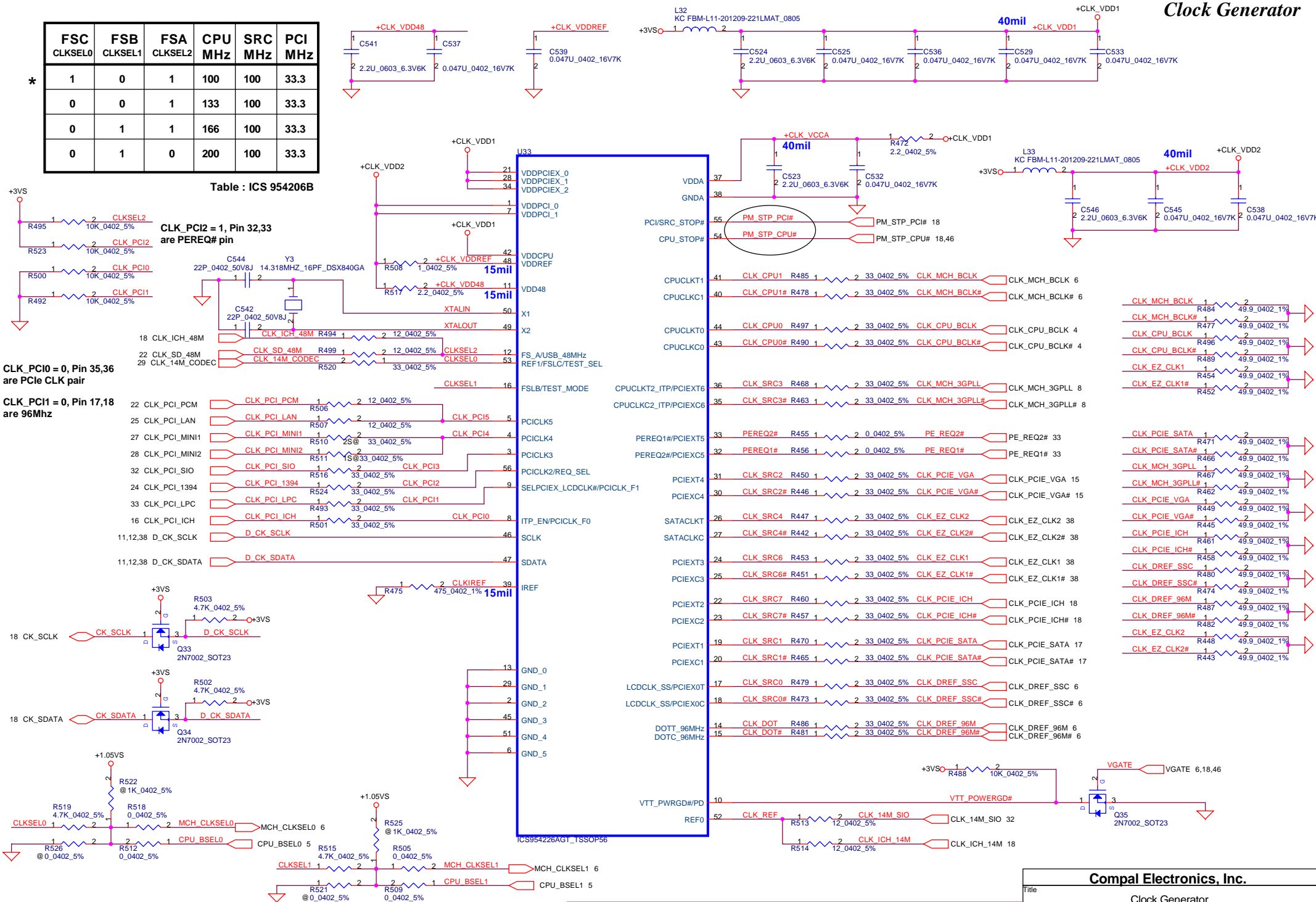
FSC CLKSEL0	FSB CLKSEL1	FSA CLKSEL2	CPU MHz	SRC MHz	PCI MHz
1	0	1	100	100	33.3
0	0	1	133	100	33.3
0	1	1	166	100	33.3
0	1	0	200	100	33.3

Table : ICS 954206B

CLK_PCI2 = 1, Pin 32,33
are PEREQ# pin

CLK_PCI0 = 0, Pin 35,36
are PCIe CLK pair

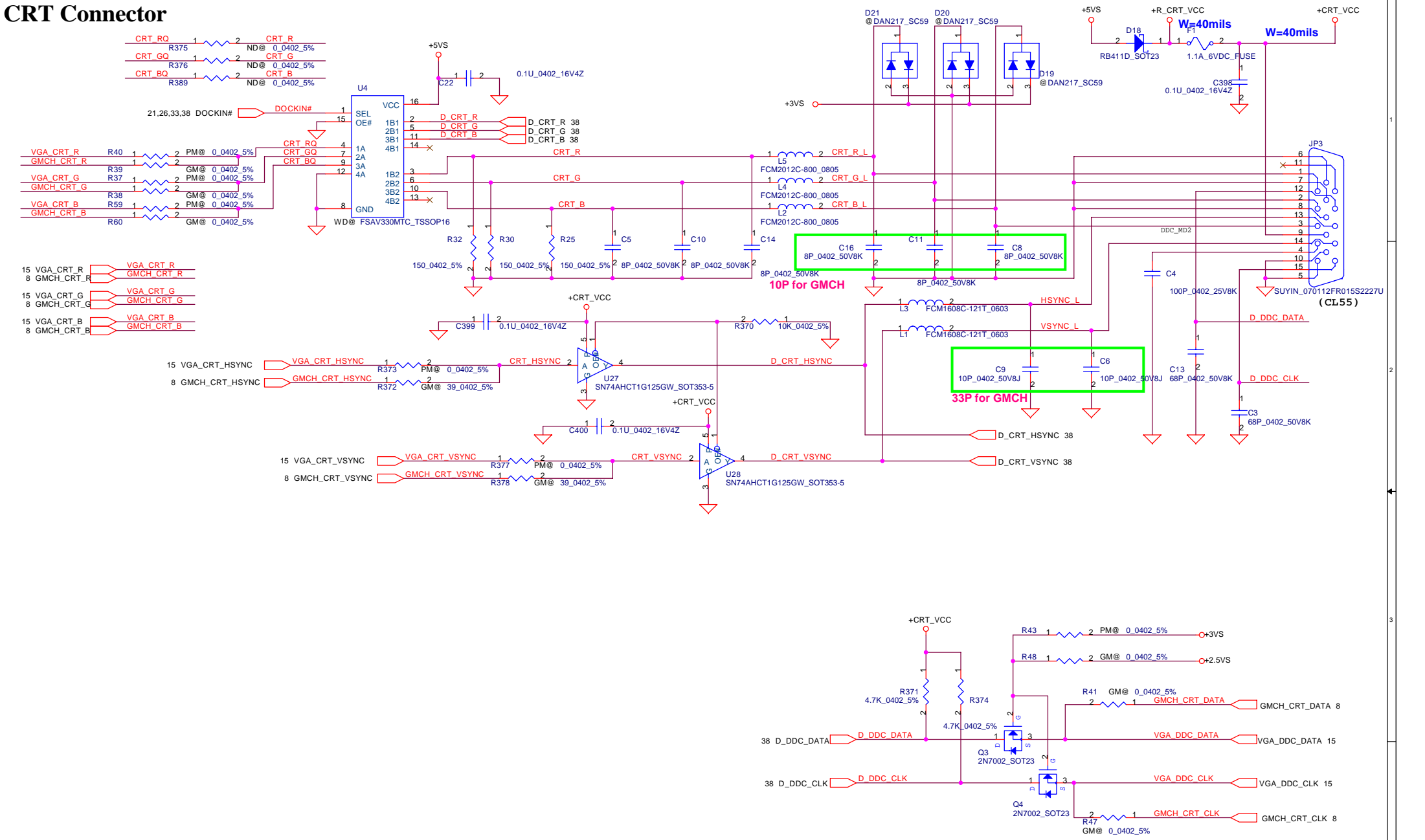
CLK_PCI1 = 0, Pin 17,18
are 96MHz



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Title			Clock Generator		
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CRT Connector

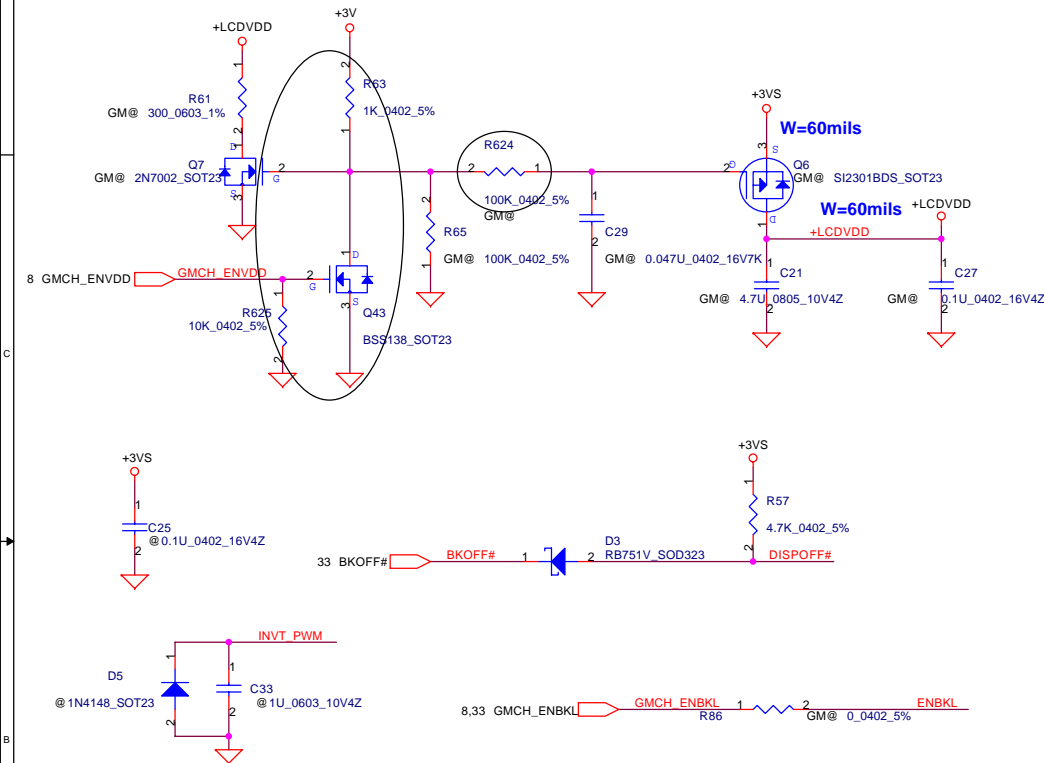


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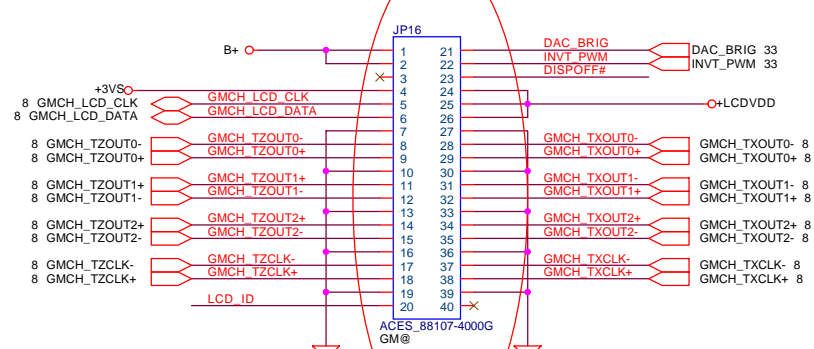
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Title			
CRT & TVout Connector			
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LCD POWER CIRCUIT



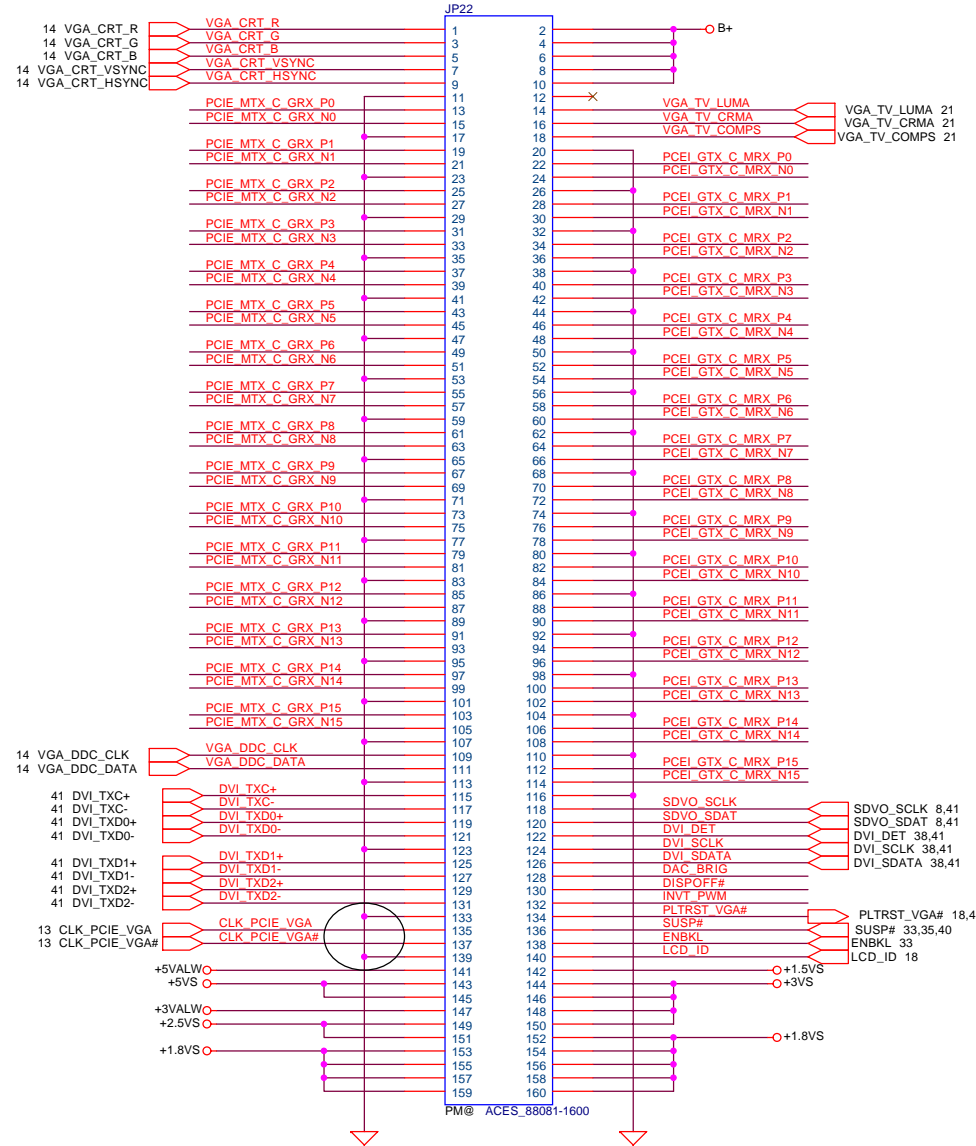
LCD/PANEL BD. Conn.



Reverse for Layout

8,41 PCIE GTX_C_MRX_N[0..15] PCIE GTX_C_MRX_N[0..15]
 8,41 PCIE GTX_C_MRX_P[0..15] PCIE GTX_C_MRX_P[0..15]
 8,41 PCIE_MTX_C_GRX_N[0..15] PCIE_MTX_C_GRX_N[0..15]
 8,41 PCIE_MTX_C_GRX_P[0..15] PCIE_MTX_C_GRX_P[0..15]

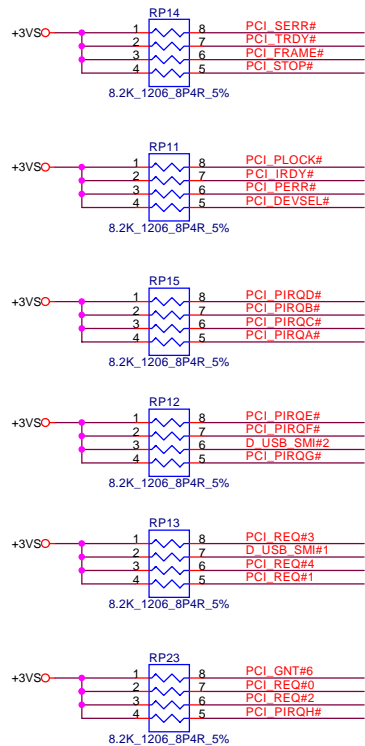
VGA BOARD Conn.



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22,24,25,27,28 PCI_AD[0..31]

- PCI_AD0 E2
- PCI_AD1 E5
- PCI_AD2 C2
- PCI_AD3 E5
- PCI_AD4 E3
- PCI_AD5 E9
- PCI_AD6 F2
- PCI_AD7 D6
- PCI_AD8 E6
- PCI_AD9 D3
- PCI_AD10 A2
- PCI_AD11 D2
- PCI_AD12 D5
- PCI_AD13 H3
- PCI_AD14 B4
- PCI_AD15 J5
- PCI_AD16 K2
- PCI_AD17 K5
- PCI_AD18 D4
- PCI_AD19 L6
- PCI_AD20 G3
- PCI_AD21 H4
- PCI_AD22 H2
- PCI_AD23 H5
- PCI_AD24 B3
- PCI_AD25 M6
- PCI_AD26 B2
- PCI_AD27 K6
- PCI_AD28 K3
- PCI_AD29 A5
- PCI_AD30 L1
- PCI_AD31 K4

U13B

PCI

- REQ[0]# C1
- GNT[0]# B5
- REQ[1]# B6
- GNT[1]# M5
- REQ[2]# F1
- GNT[2]# B8
- REQ[3]# C8
- GNT[3]# F7
- REQ[4]#/GPI[40] E7
- GNT[4]#/GPO[48] E8
- REQ[5]#/GPI[1] F6
- GNT[5]#/GPO[17] B7
- REQ[6]#/GPI[0] D8
- GNT[6]#/GPO[16] J6
- C/BE[0]# H6
- C/BE[1]# G4
- C/BE[2]# G2
- C/BE[3]# A3
- IRDY# E1
- PAR R2
- PCIRST# C3
- DEVSEL# E3
- PERR# C5
- PLOCK# G5
- SERR# J1
- STOP# J2
- TRDY# J2
- PLTRST# R5
- PCICLK G6
- PME# P6
- REQ#0 L5
- GNT#0 C1
- REQ#1 B6
- GNT#1 M5
- REQ#2 F1
- GNT#2 B8
- REQ#3 C8
- GNT#3 F7
- D_USB_SMI#1 E8
- PCI_GNT#5 F6
- D_USB_SMI#2 B7
- PCI_GNT#6 J6
- PCI_CBE#0 H6
- PCI_CBE#1 G4
- PCI_CBE#2 G2
- PCI_CBE#3 A3
- PCI_IRDY# E1
- PCI_PAR R2
- PCI_RST# C3
- PCI_DEVSEL# E3
- PCI_PERR# C5
- PCI_PLOCK# G5
- PCI_SERR# J1
- PCI_STOP# J2
- PCI_TRDY# J2
- PLT_RST# R5
- CLK_ICH_PCI G6
- CLK_PCI_ICH P6
- PCI_REQ#0 24
- PCI_GNT#0 24
- PCI_REQ#1 27,28
- PCI_GNT#1 27,28
- PCI_REQ#2 22
- PCI_GNT#2 22
- PCI_REQ#3 25
- PCI_GNT#3 25
- D_USB_SMI#1 38
- PCI_GNT#5 22,24,25,27,28
- D_USB_SMI#2 22,24,25,27,28
- PCI_CBE#0 22,24,25,27,28
- PCI_CBE#1 22,24,25,27,28
- PCI_CBE#2 22,24,25,27,28
- PCI_CBE#3 22,24,25,27,28
- PCI_IRDY# 22,24,25,27,28
- PCI_PAR 22,24,25,27,28
- PCI_RST# 22,24,25,27,28,32,33
- PCI_DEVSEL# 22,24,25,27,28
- PCI_PERR# 22,24,25,27,28
- PCI_PLOCK# 22,24,25,27,28
- PCI_SERR# 22,24,25,27,28
- PCI_STOP# 22,24,25,27,28
- PCI_TRDY# 22,24,25,27,28
- PLT_RST# 6,18,20,32,33,41
- CLK_PCI_ICH 13
- PCI_PIRQE# D9
- PCI_PIRQF# C7
- PCI_PIRQG# C6
- PCI_PIRQH# M3
- PCI_PIRQA# 24
- PCI_PIRQF# 25
- PCI_PIRQG# 27,28
- PCI_PIRQH# 27,28

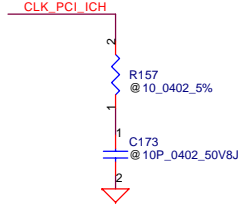
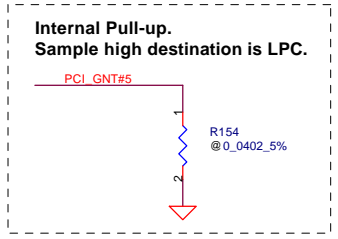
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- PIRQ[A]# L2
- PIRQ[B]# L2
- PIRQ[C]# M1
- PIRQ[D]# L3
- PIRQ[E]#/GPI[2] D9
- PIRQ[F]#/GPI[3] C7
- PIRQ[G]#/GPI[4] C6
- PIRQ[H]#/GPI[5] M3
- PCI_PIRQA# N2
- PCI_PIRQB# M1
- PCI_PIRQC# L3
- PCI_PIRQD# L3

RESERVED

- AC5
- AD5
- AF4
- AG4
- AD9
- AF8
- AG8
- U3
- SATA[1]RXN/RSVD[1]
- SATA[1]RXP/RSVD[2]
- SATA[1]TXN/RSVD[3]
- SATA[1]TXP/RSVD[4]
- SATA[3]RXN/RSVD[5]
- SATA[3]RXP/RSVD[6]
- SATA[3]TXN/RSVD[7]
- SATA[3]TXP/RSVD[8]
- TP[3]RSVD[9]

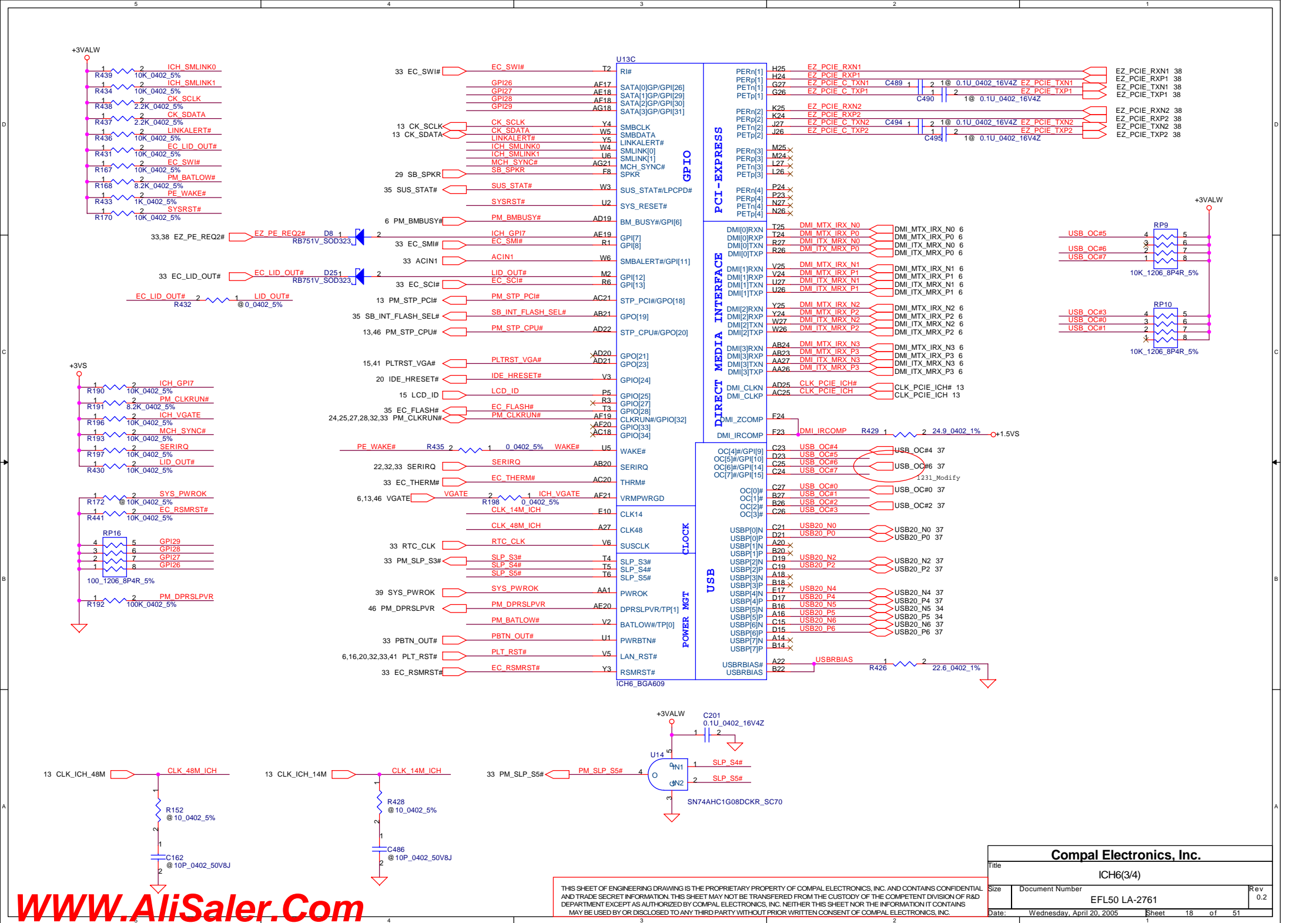
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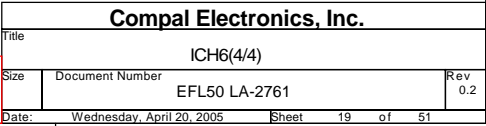


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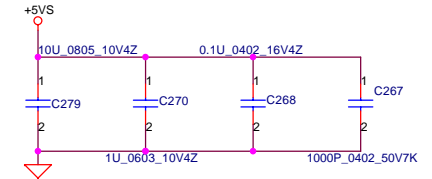
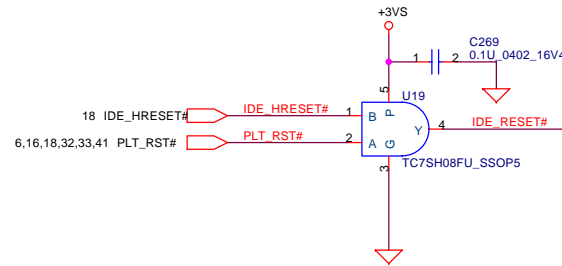
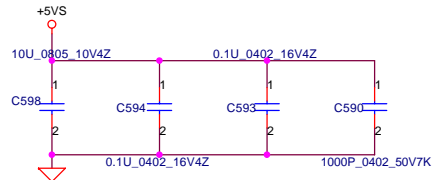
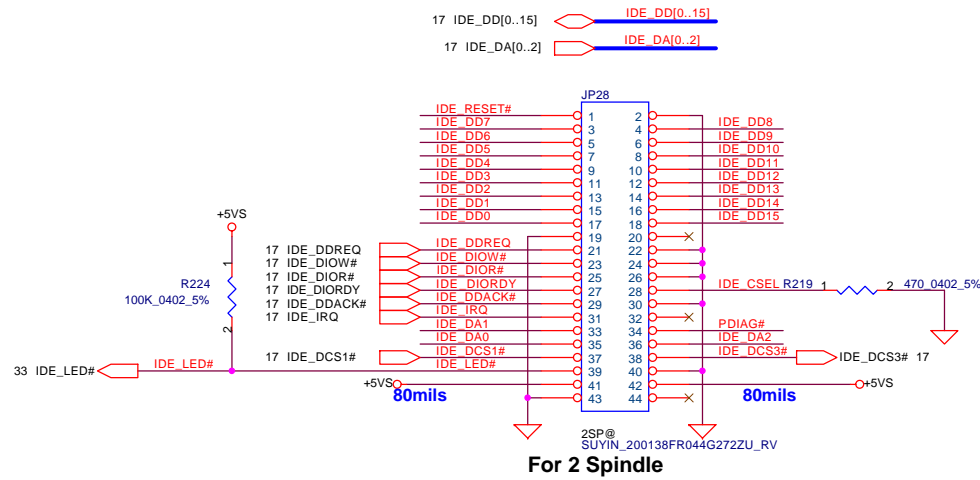
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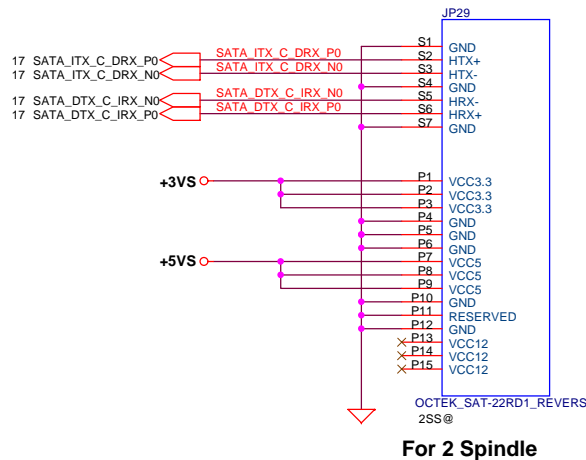




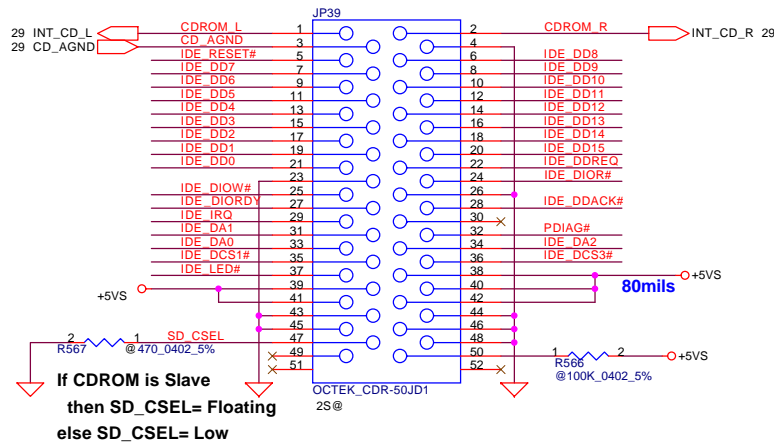
HDD CONN



SATA HDD CONN



CDROM CONN

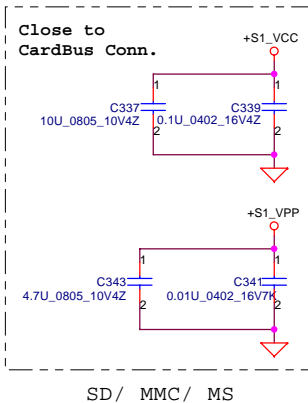
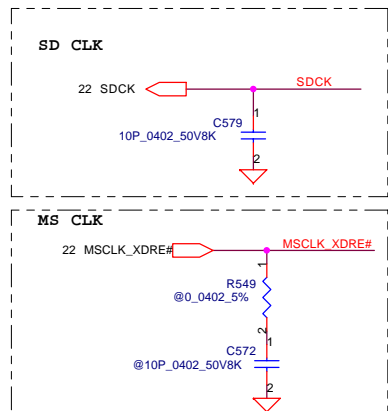
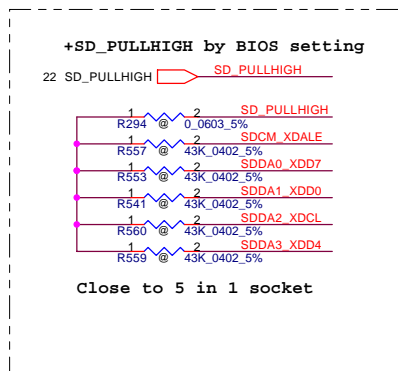
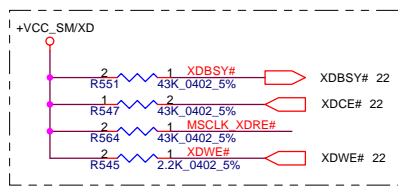
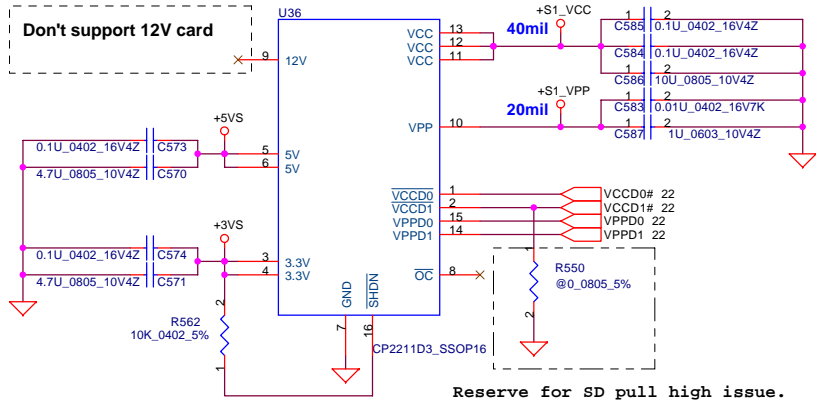


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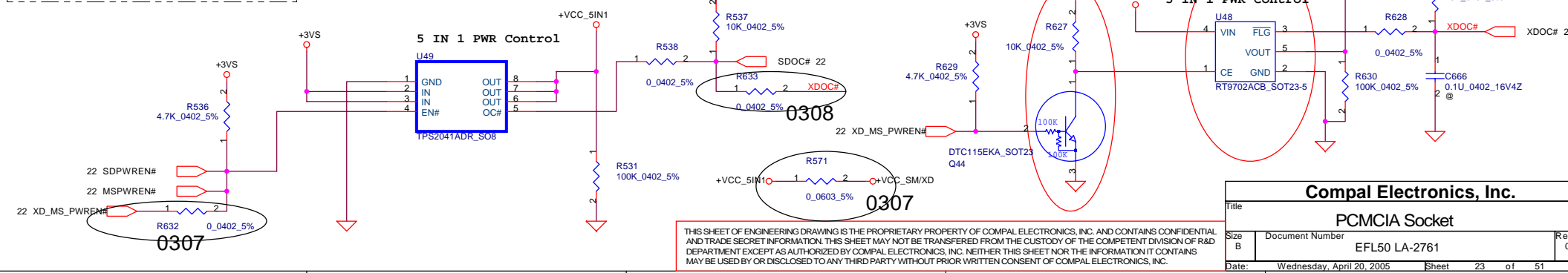
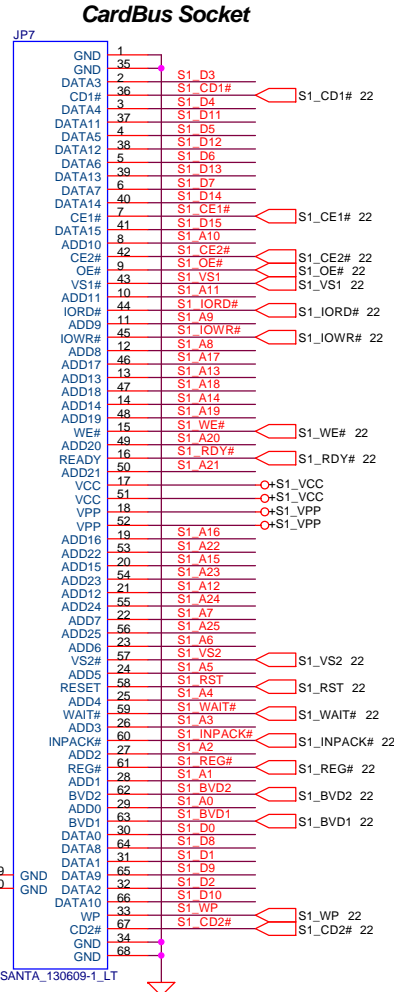
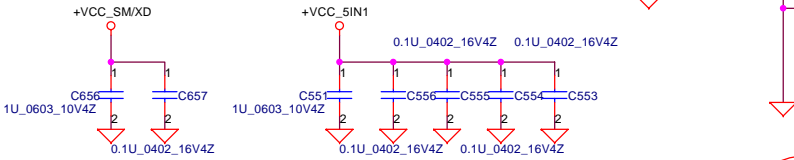
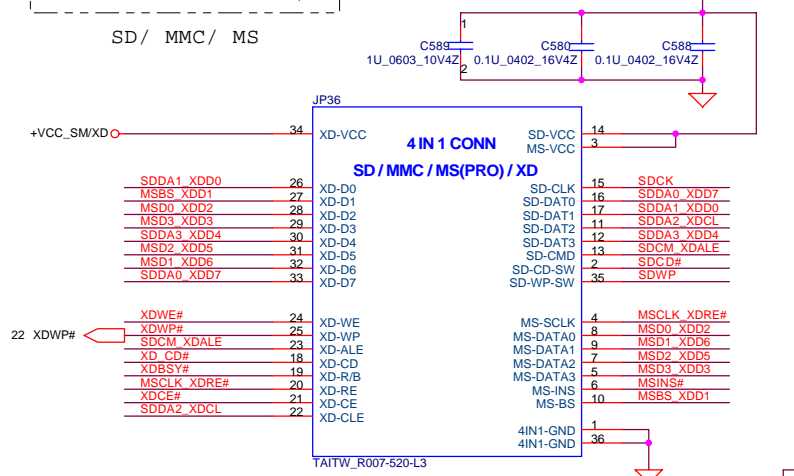
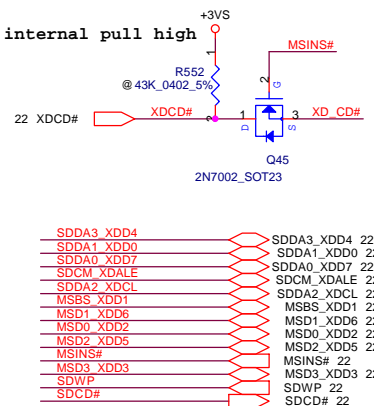
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PCMCIA Power Controller

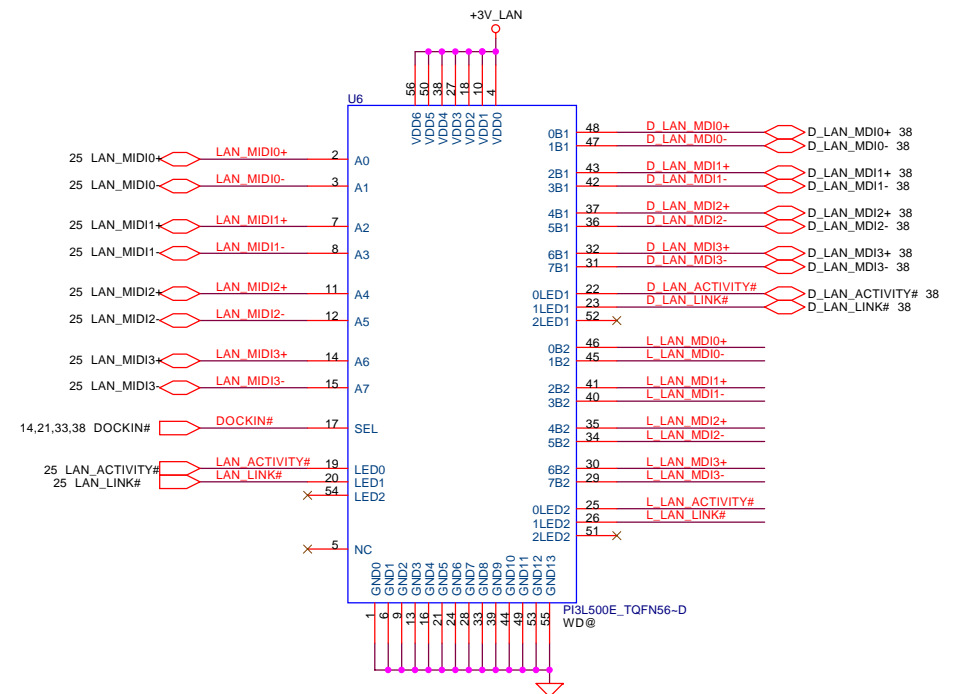


Chip has internal pull high



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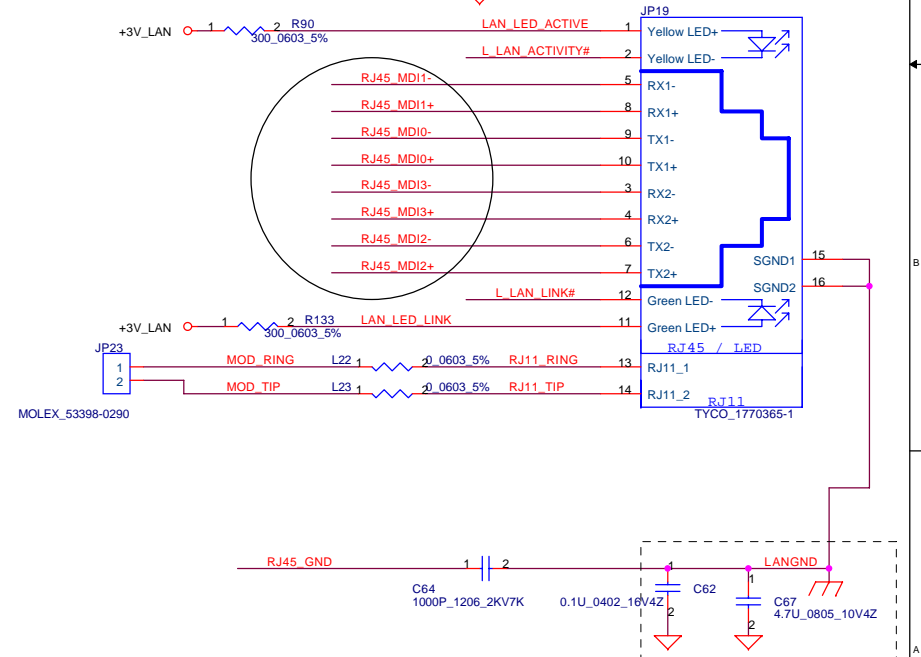
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PCMCIA Socket			
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LAN BCM5788M/BCM4401KFB

24ST0023-3(SP050004200) for BCM4401(10/100)

24HST1041A-3(SP050002110) for BCM5788M(GbE)



reserved for BCM4401(10/100)

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LAN Magnetic & RJ45/RJ11

Title

Size

Document Number

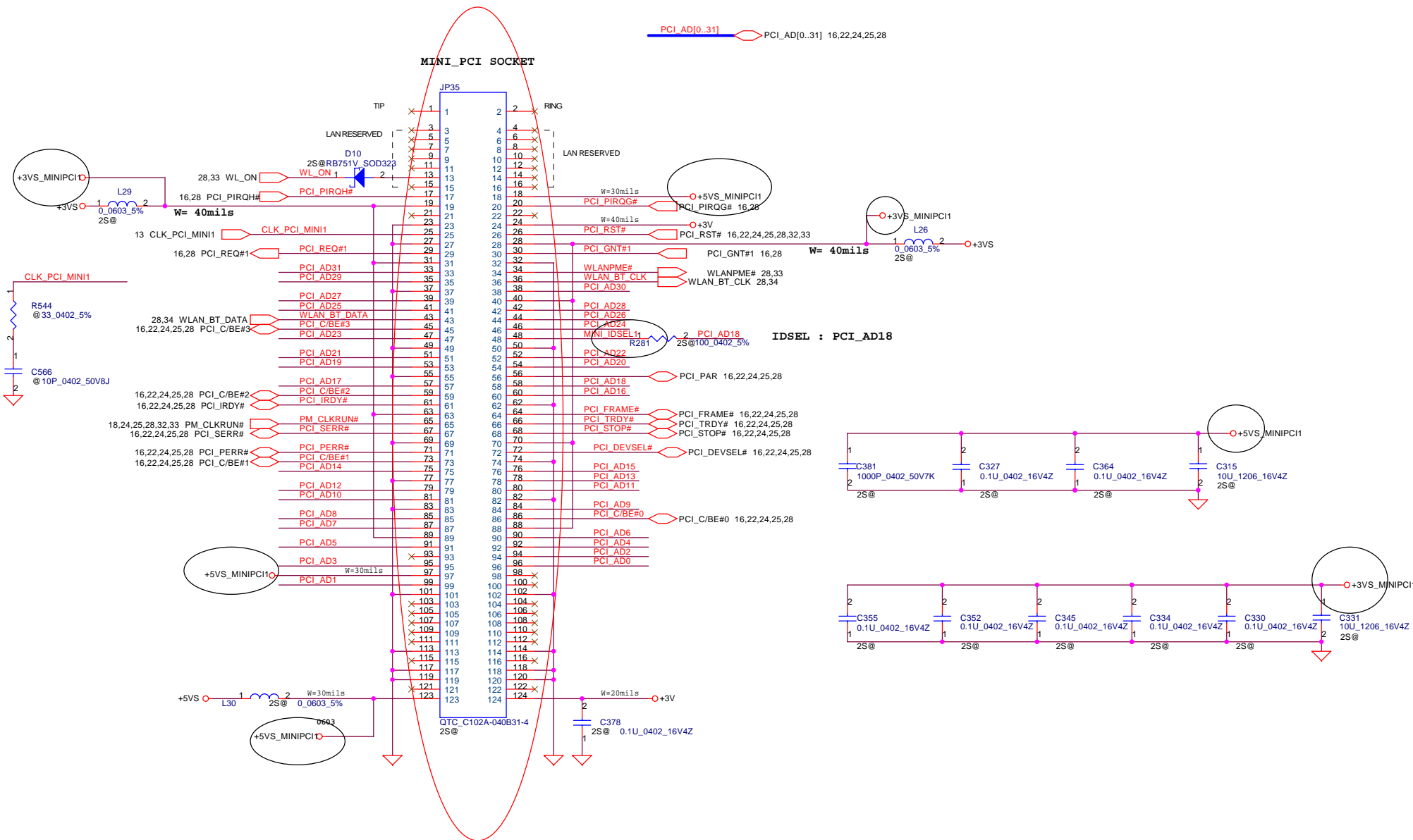
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Date:

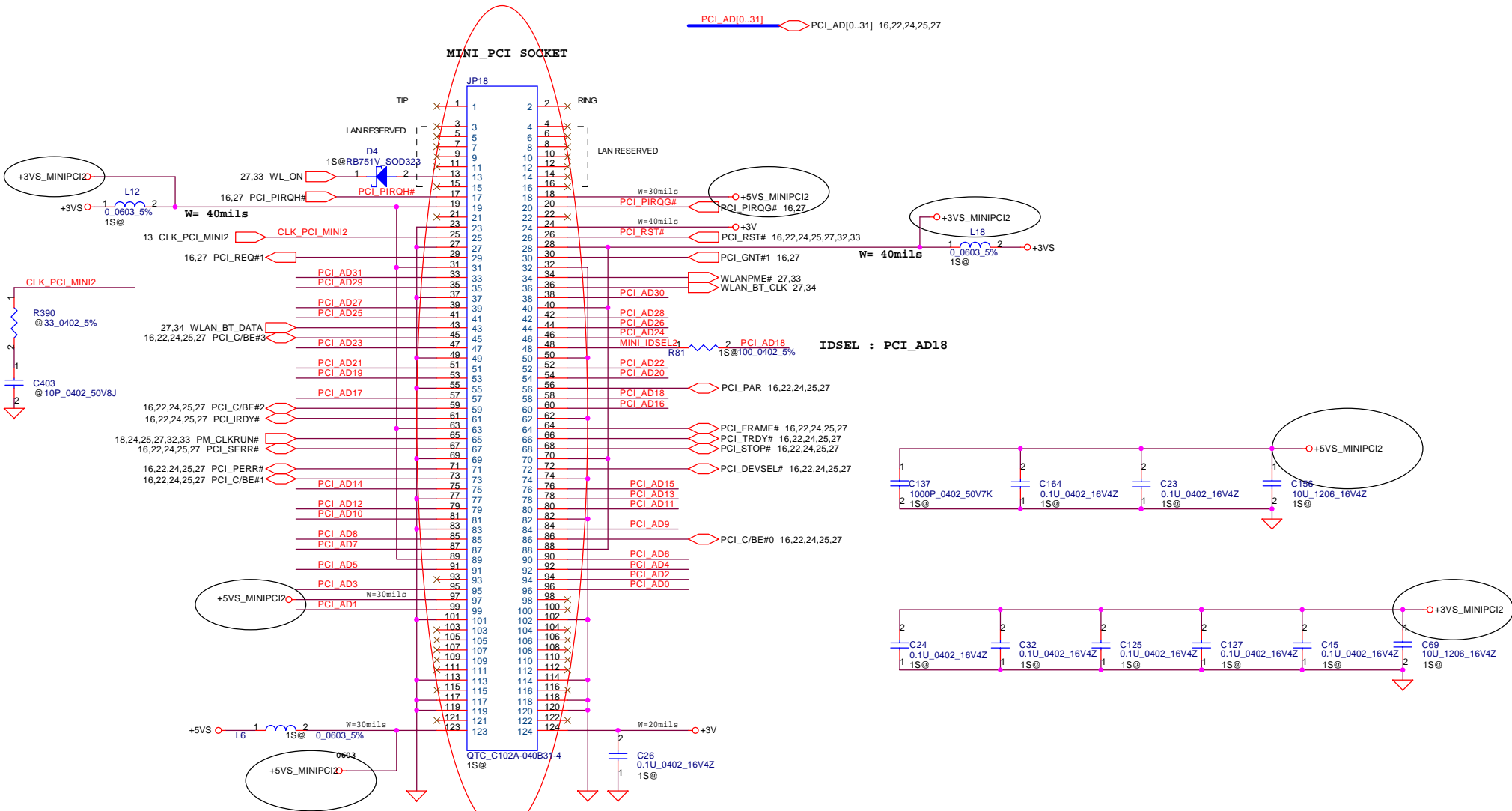
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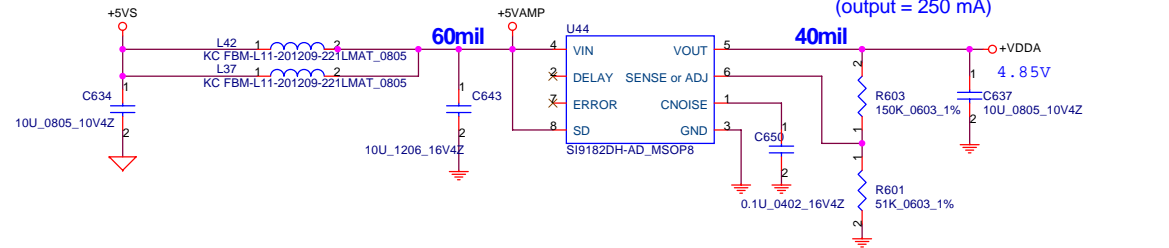
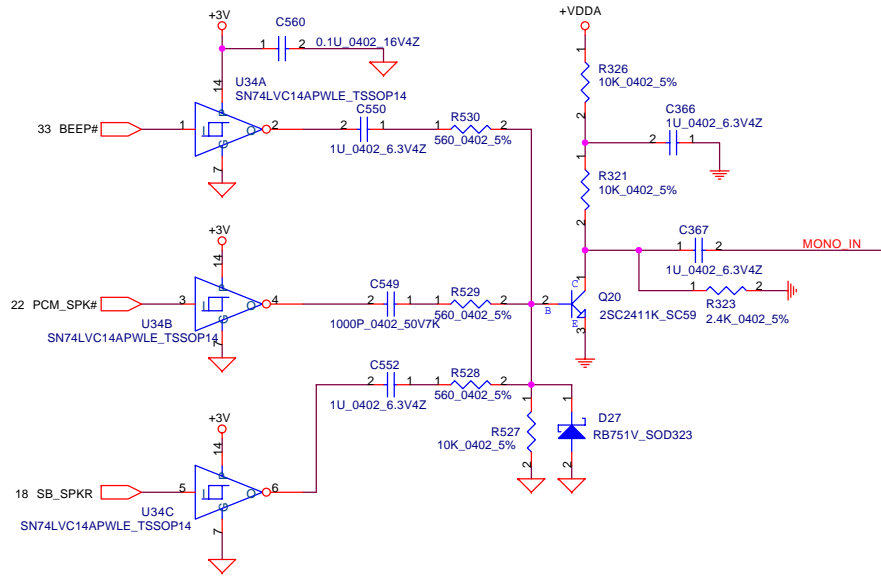
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Mini PCI Slot			
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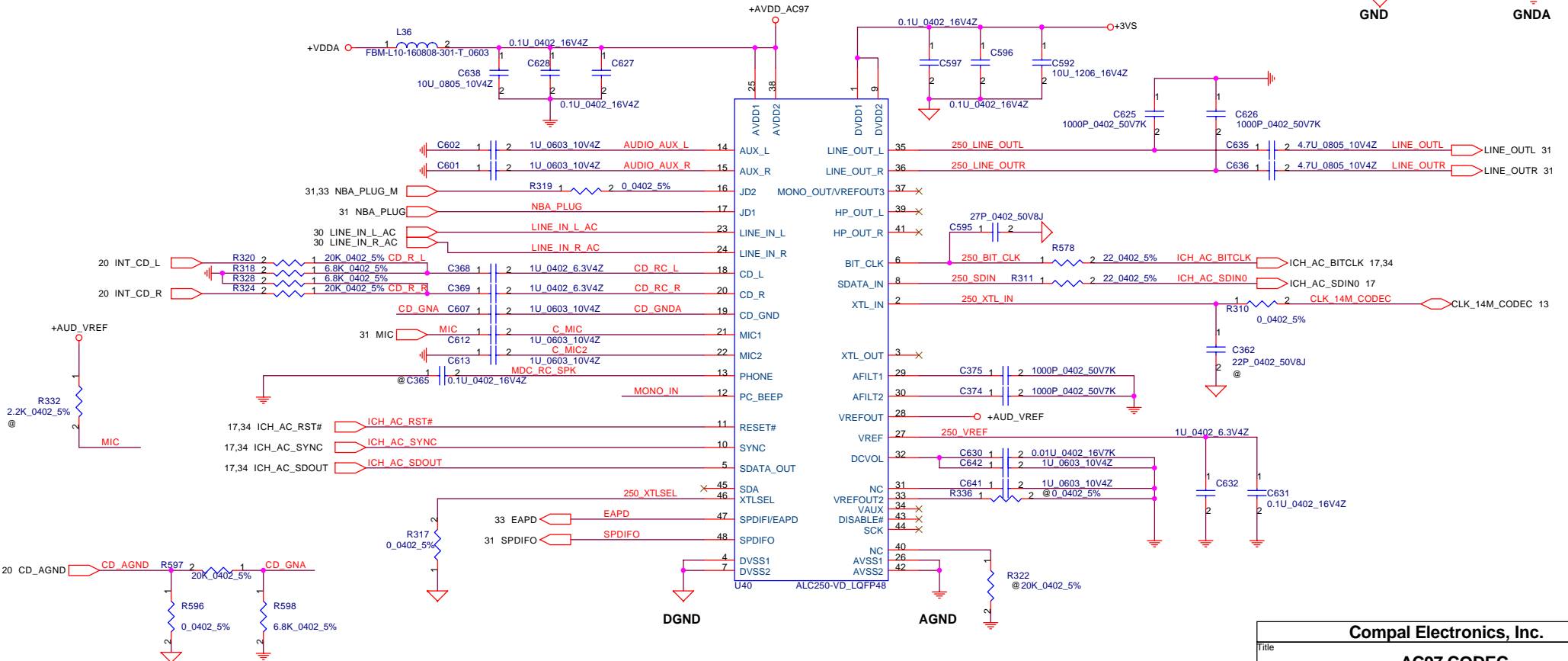


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AC97 Codec

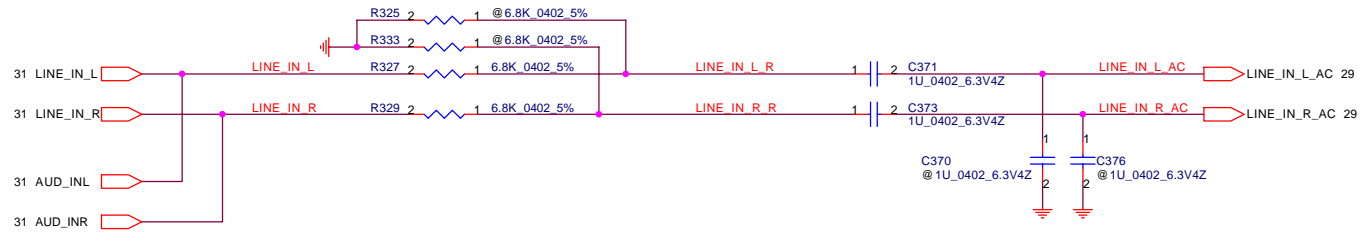


With 14.318Mhz : R321 POP
With 24.576Mhz : R321 DEPOP

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AC97 CODEC		
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Title		
Audio Line Switch		
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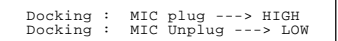
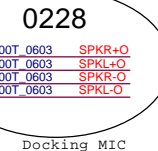
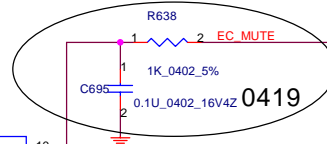
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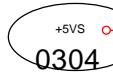
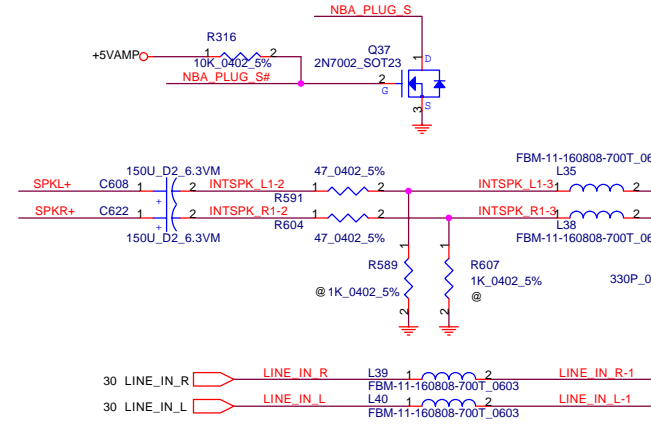
JP9

SPKR+O	4	4
SPKR-O	3	3
SPKL+O	2	2
SPKL-O	1	1

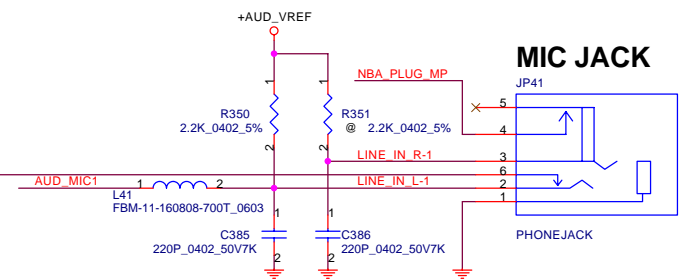
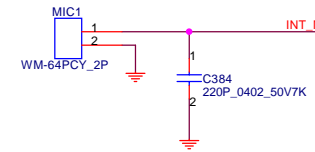
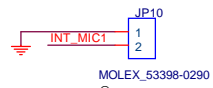
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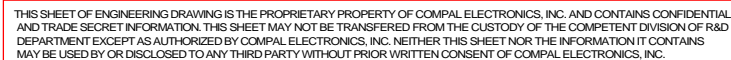
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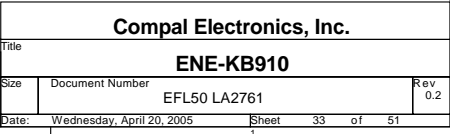


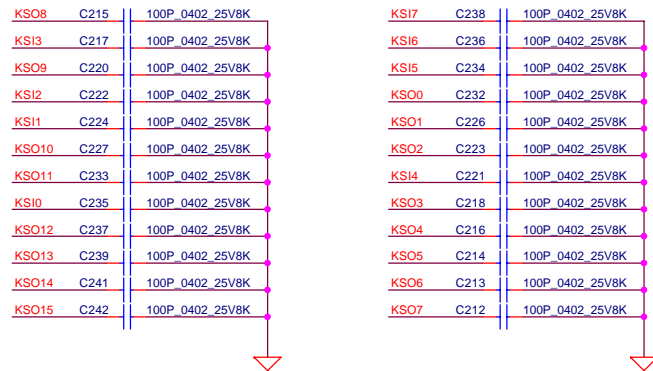
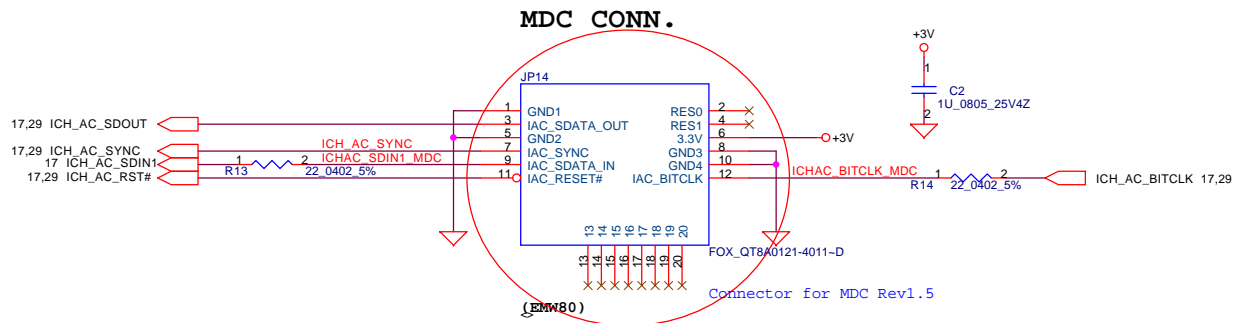
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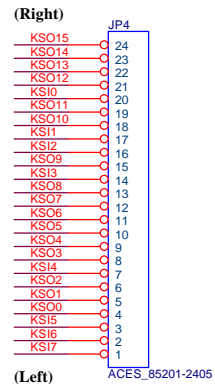
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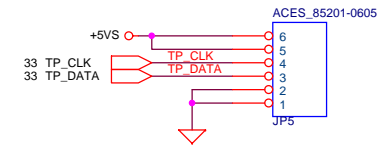




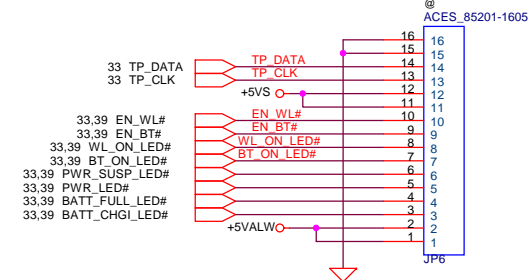
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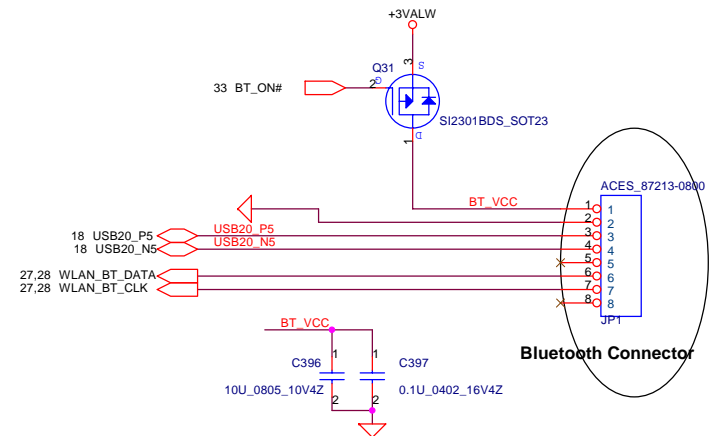
TO M/B



TO M/B



BlueTooth Interface

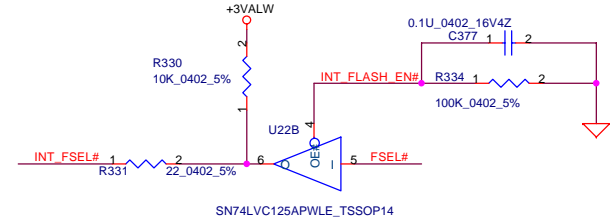
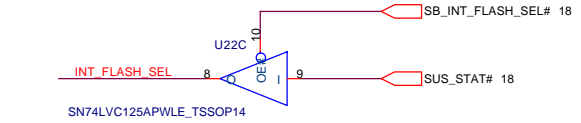
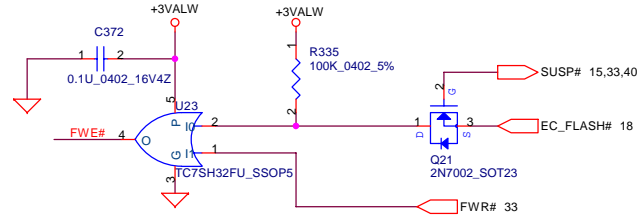
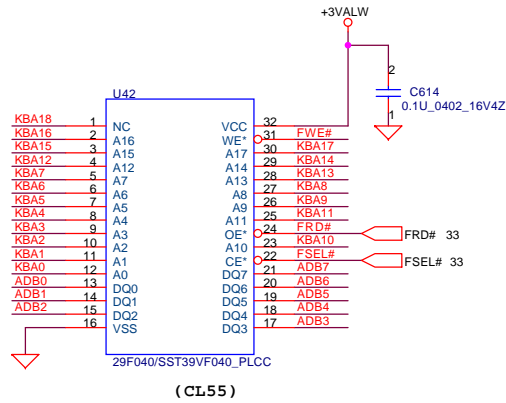


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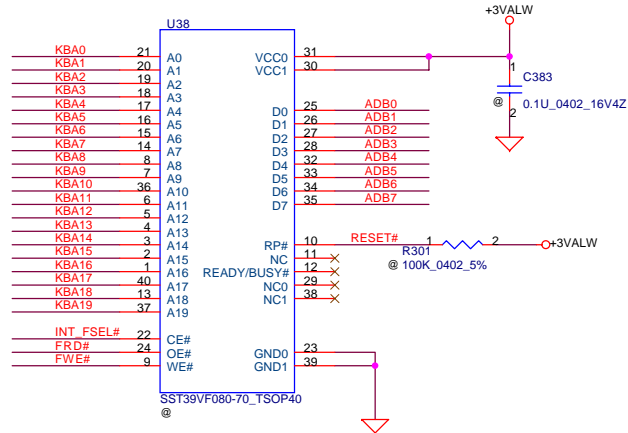
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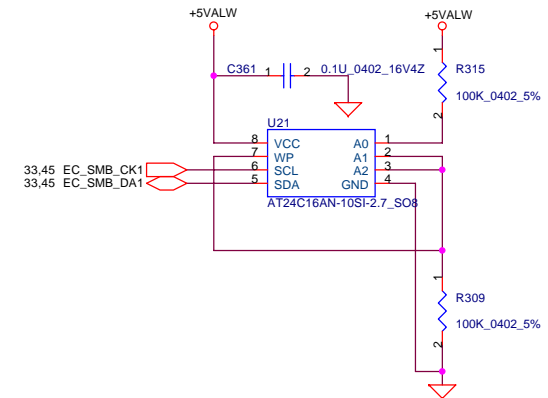
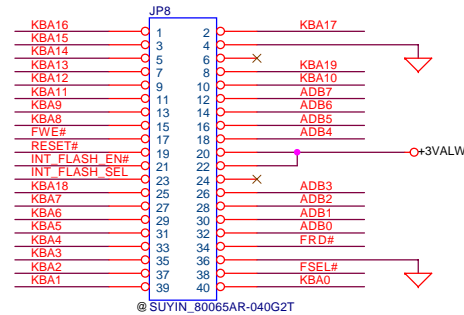
33 KBA[0..19] KBA[0..19]
33 ADB[0..7] ADB[0..7]



1MB Flash ROM



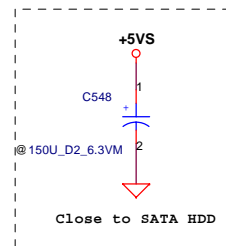
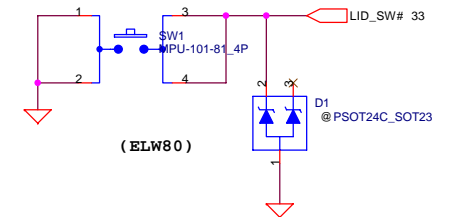
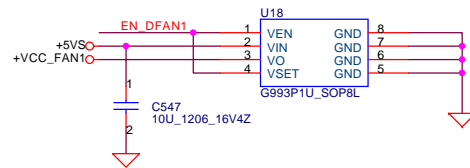
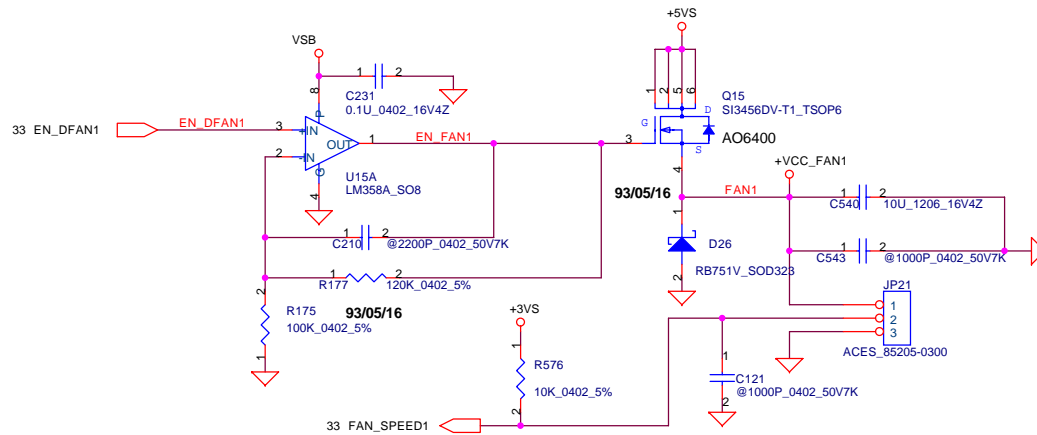
1MB ROM Socket



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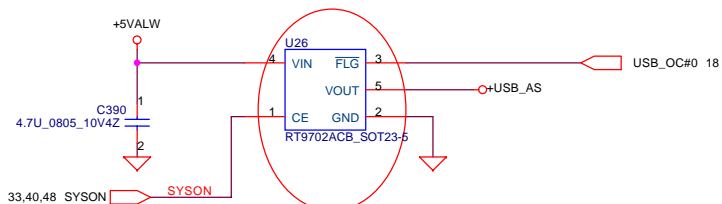
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BIOS & EXT. I/O PORT & SATA HDD			
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FAN Conn

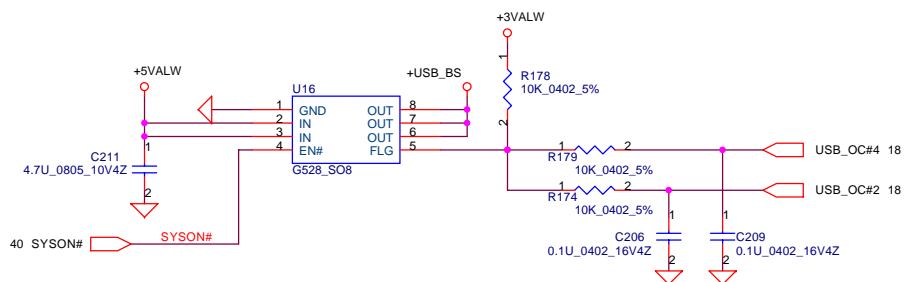


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Compal Electronics, Inc.			
Title			
FAN / FIR / RJ11/Lid Switch			
Size	Document Number		Rev
	EFL50 LA-2761		0.2
Date:	Wednesday, April 20, 2005	Sheet	36 of 51

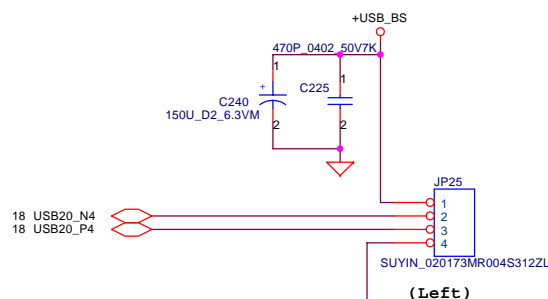
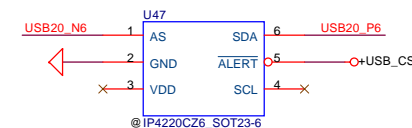
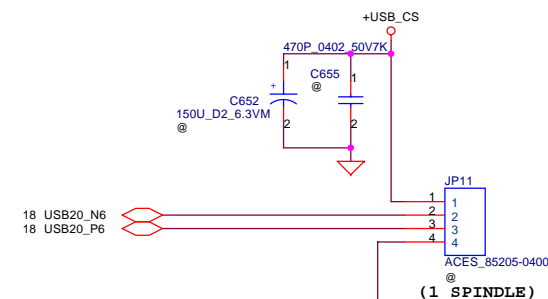
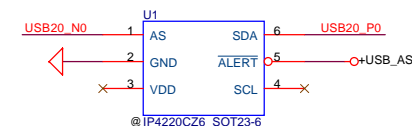
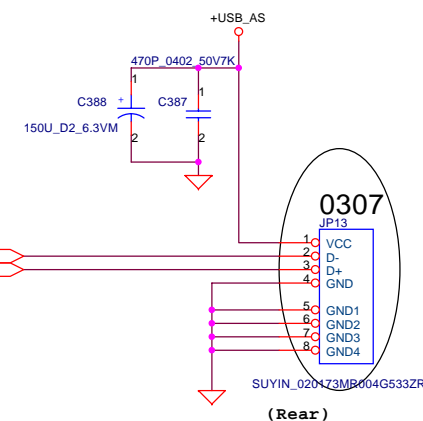
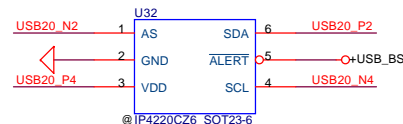
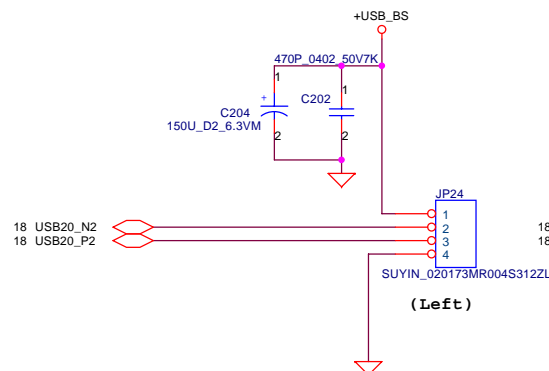
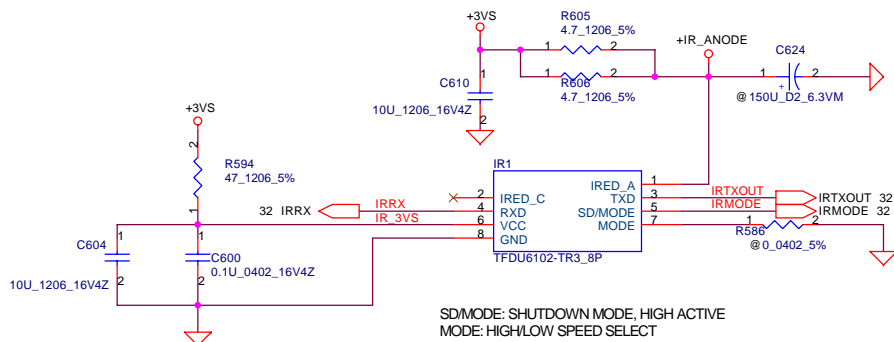


Cost-Down from G528 to RT9702



Cost-Down from G528 to RT9702

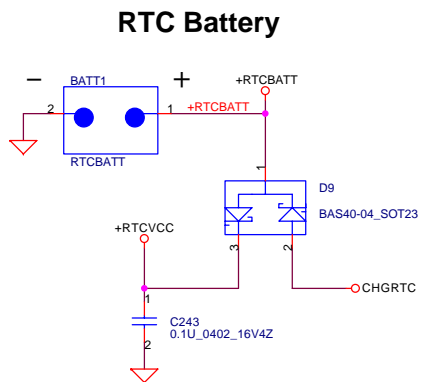
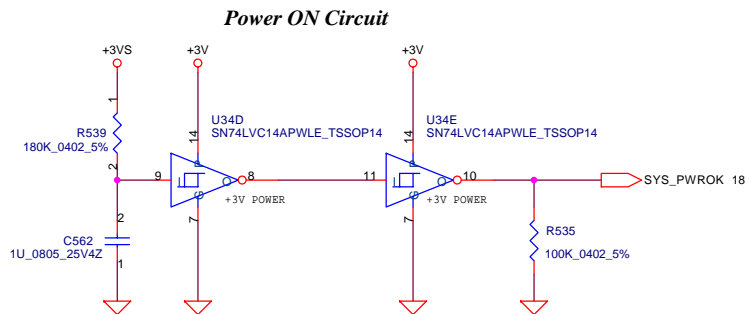
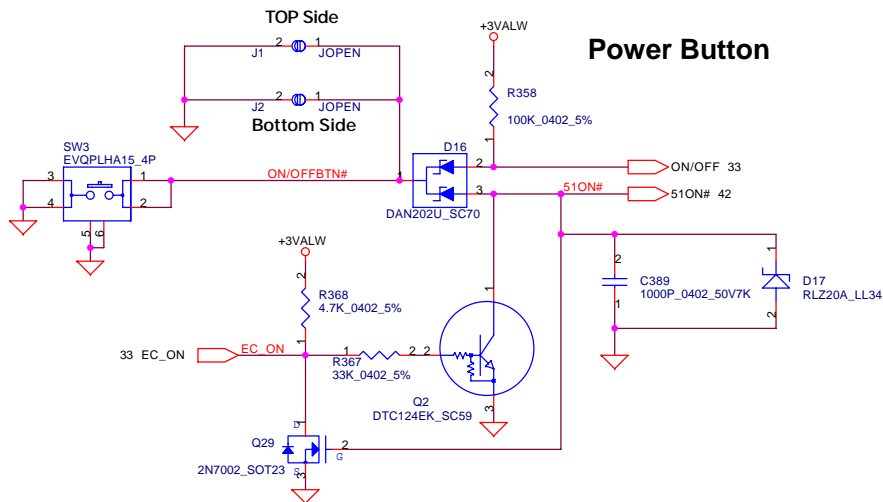
FIR Module



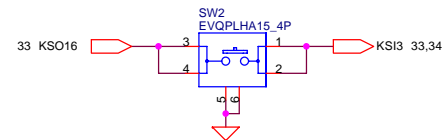
Compal Electronics, Inc.

Title		
USB Conn		
Size	Document Number	Rev
	EFL50 LA-2761	0.2
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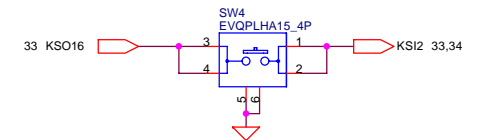
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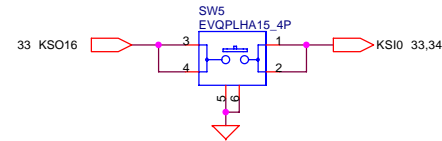
E-Mail_BTN



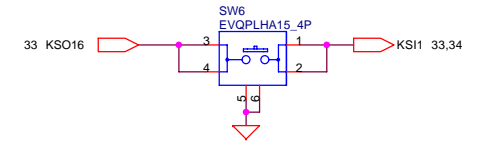
Internet_BTN



e/eManager_BTN



Launch Manager_BTN



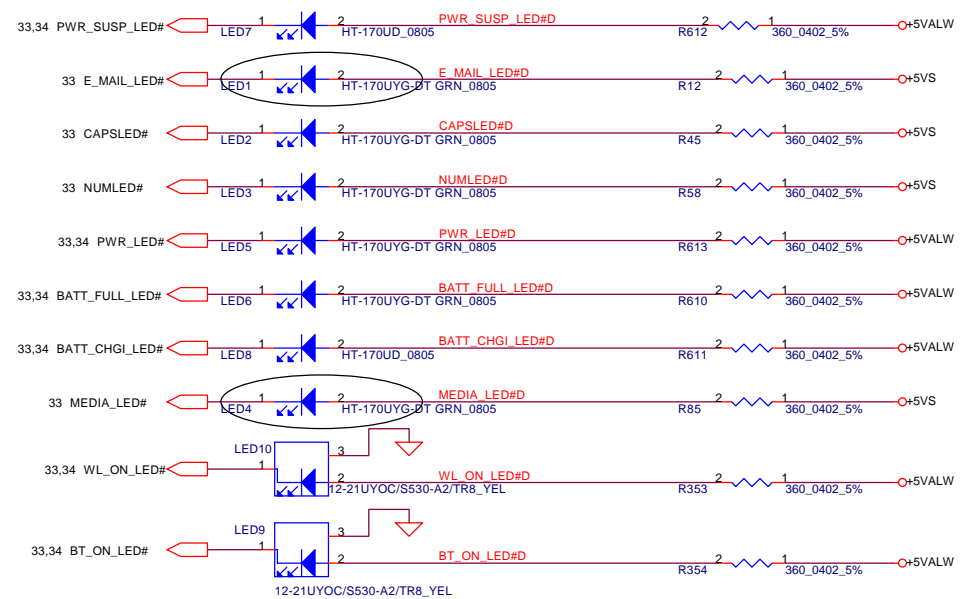
BlueTooth_BTN



Wireless_BTN

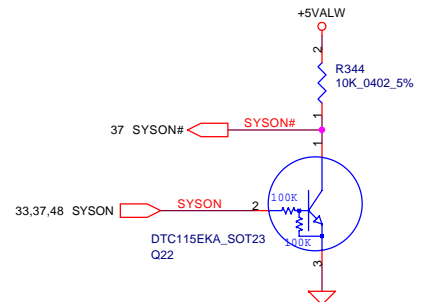
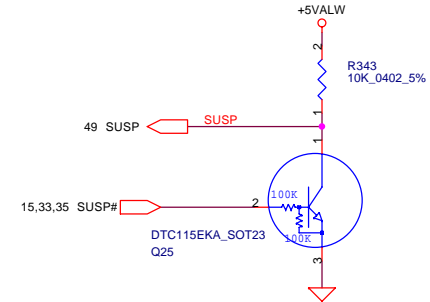
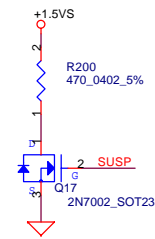
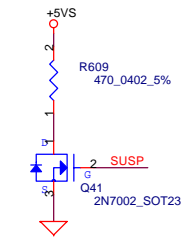
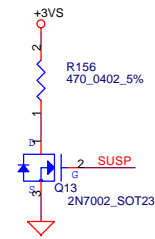
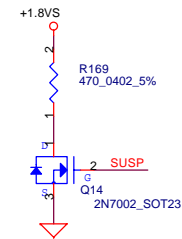
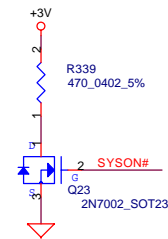
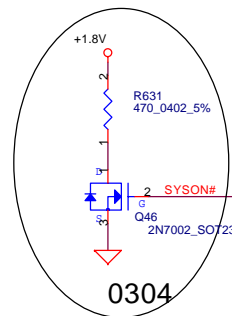
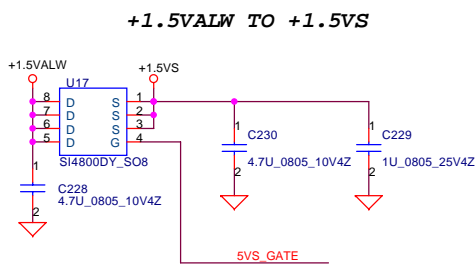
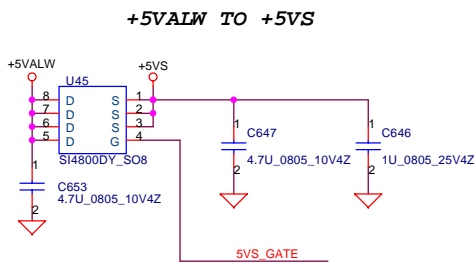
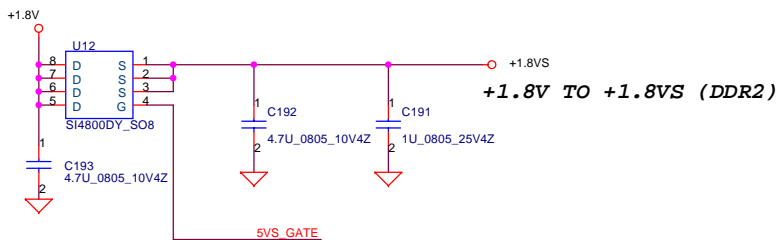
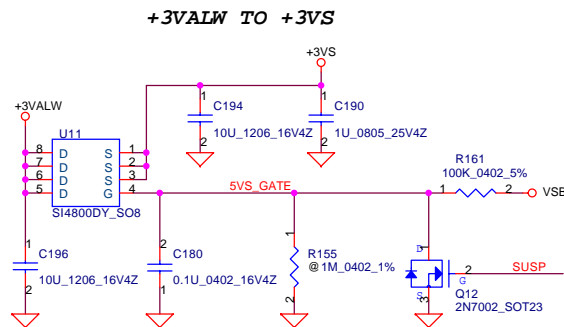
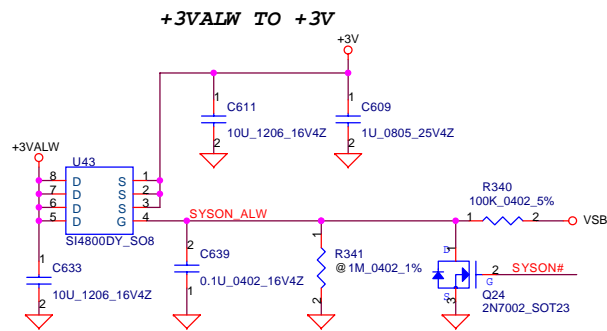


LED Indicator



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Compal Electronics, Inc.			
Title			
Power OK/Reset/RTC battery			
Size	Document Number	Rev	
	EFL50 LA-2761	0.2	
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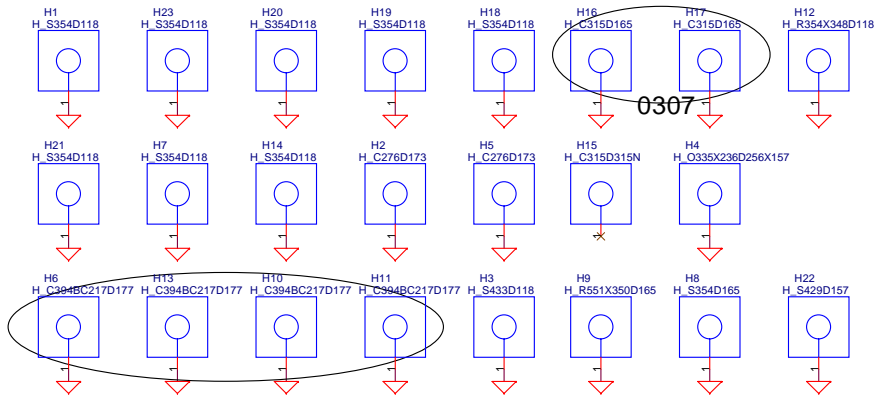
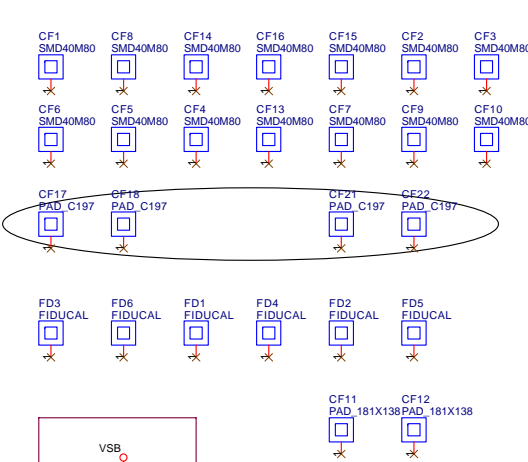


Compal Electronics, Inc.

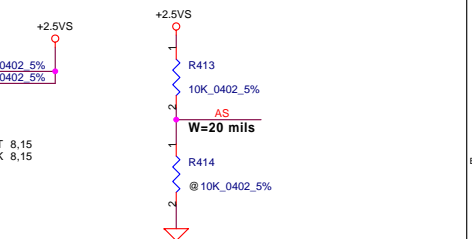
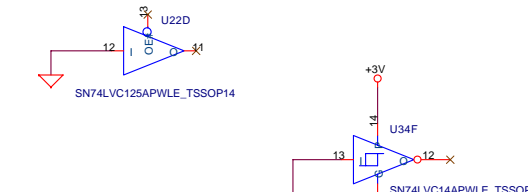
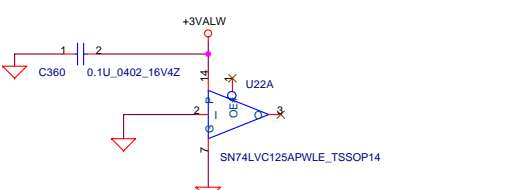
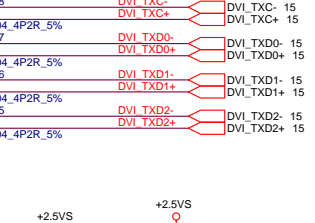
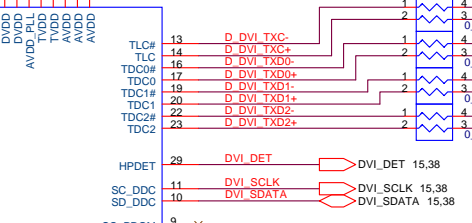
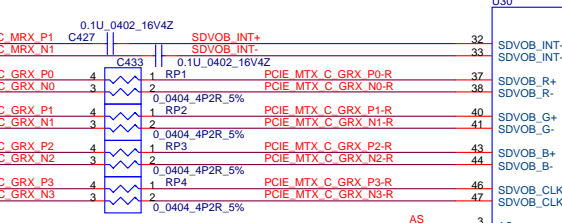
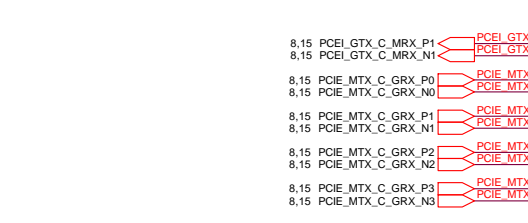
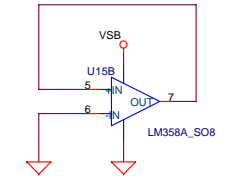
POWER CONTROL CKT

Size	Document Number	Rev
B	EFL50 LA-2761	0.2
Date:	Wednesday, April 20, 2005	Sheet 40 of 51

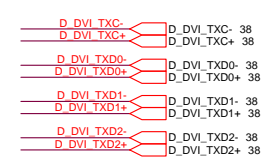
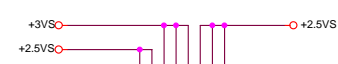
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Chip_Name	Label_Name
EXP_TXP0/SDVOB_RED	PCIE_MTX_C_GRX_P0
EXP_TXN0/SDVOB_RED#	PCIE_MTX_C_GRX_N0
EXP_TXP1/SDVOB_GREEN	PCIE_MTX_C_GRX_P1
EXP_TXN1/SDVOB_GREEN#	PCIE_MTX_C_GRX_N1
EXP_TXP2/SDVOB_BLUE	PCIE_MTX_C_GRX_P2
EXP_TXN2/SDVOB_BLUE#	PCIE_MTX_C_GRX_N2
EXP_TXP3/SDVOB_CLKP	PCIE_MTX_C_GRX_P3
EXP_TXN3/SDVOB_CLKN	PCIE_MTX_C_GRX_N3
EXP_RXP1/SDVO_INT	PCIE GTX_C_MRX_P1
EXP_RXN1/SDVO_INT#	PCIE GTX_C_MRX_N1

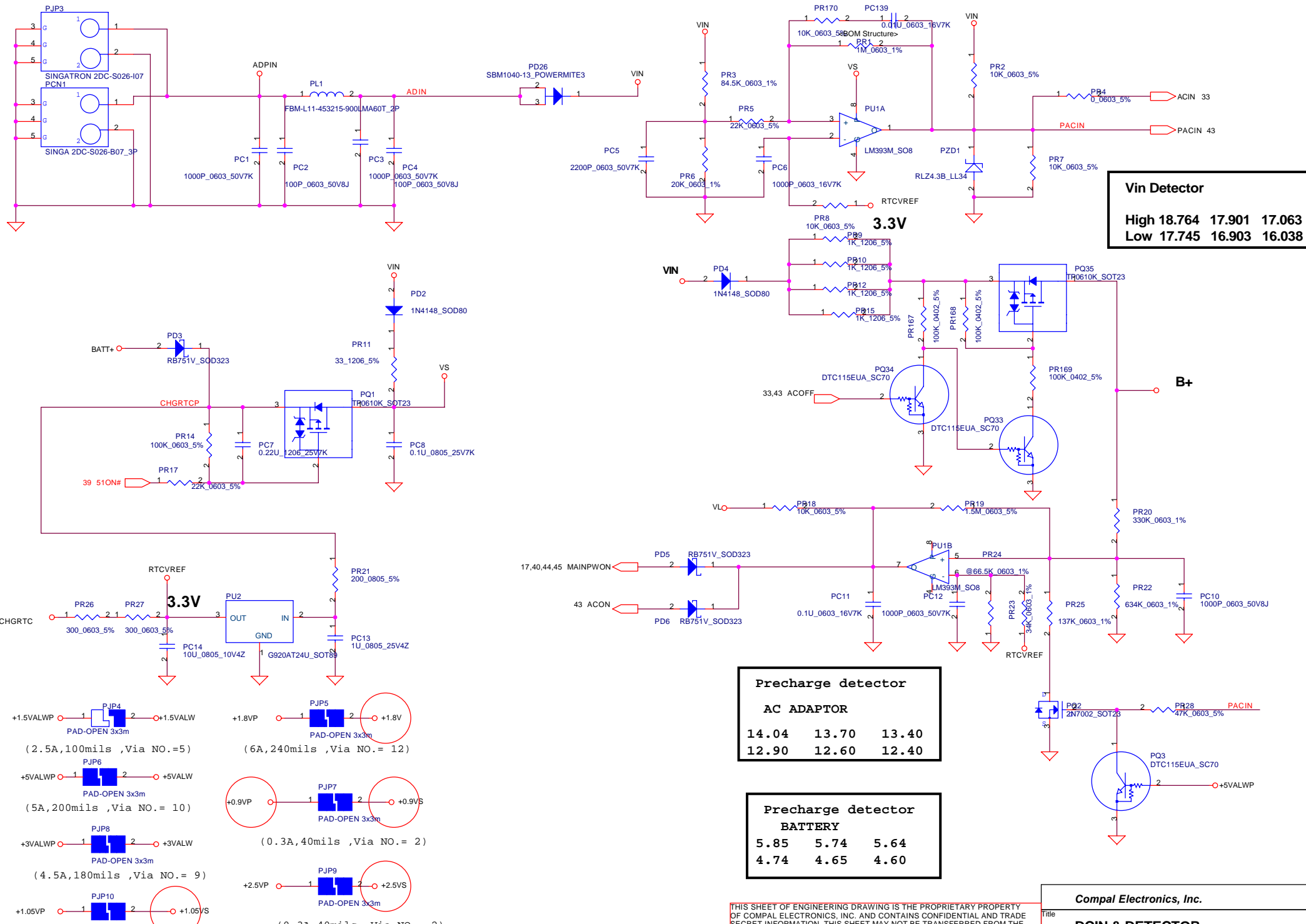


DVI CONTROLLER



Compal Electronics, Inc.		
Screws and CH7307		
Size	Document Number	Rev
	EFL50 LA-2761	0.2
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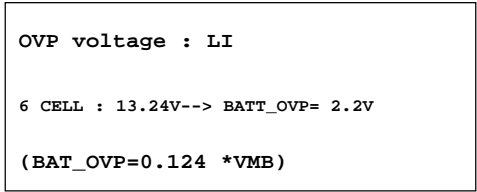
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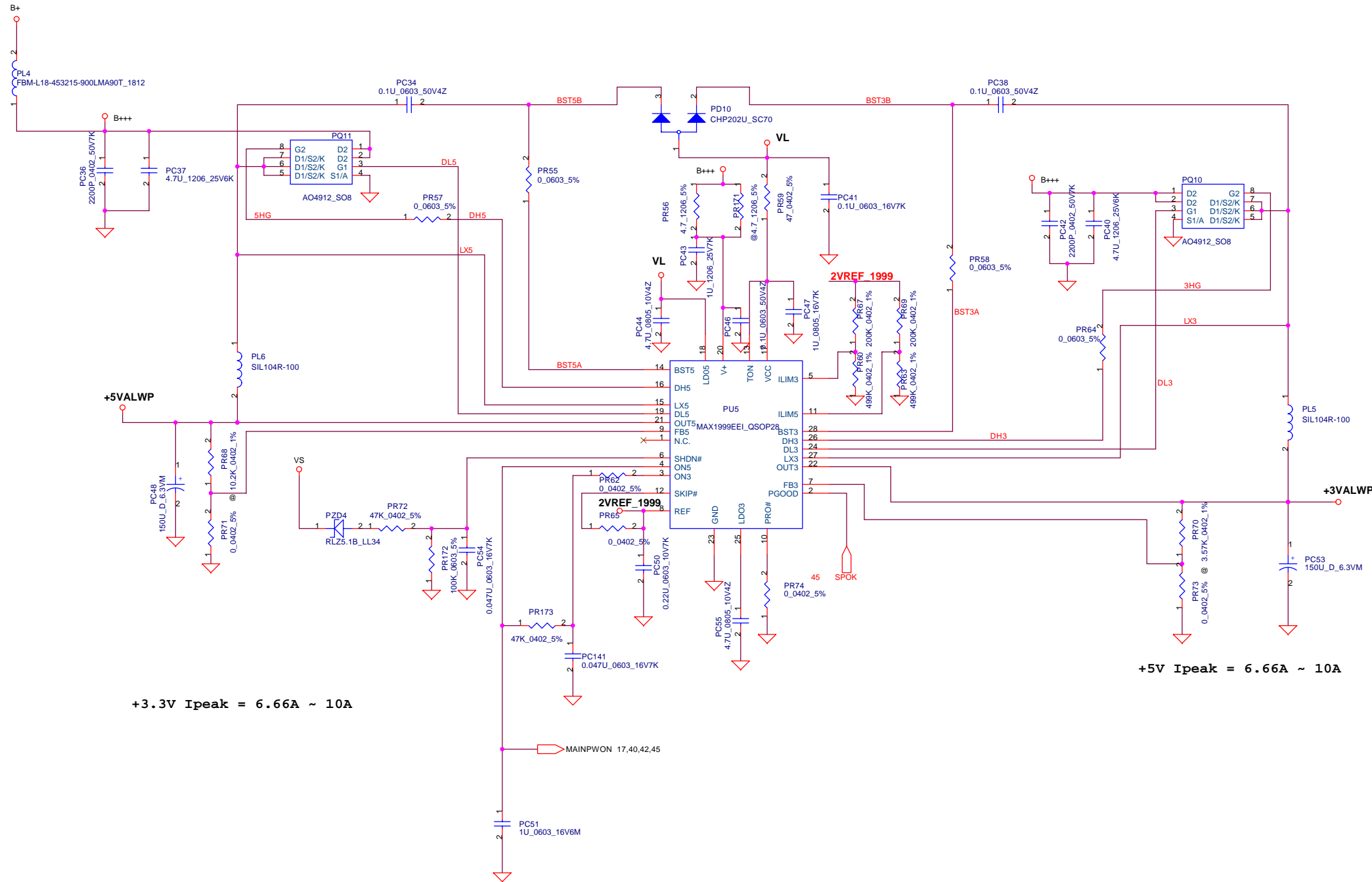
Vin Detector		
High	18.764	17.901 17.063
Low	17.745	16.903 16.038

Precharge detector		
AC ADAPTOR		
14.04	13.70	13.40
12.90	12.60	12.40

Precharge detector		
BATTERY		
5.85	5.74	5.64
4.74	4.65	4.60



Compal Electronics, Inc.				
Title				
CHARGER				
Size	Document Number			Rev
B	EFL50 LA-2761			0.1
Date:	Wednesday, April 20, 2005	Sheet	43 of 51	

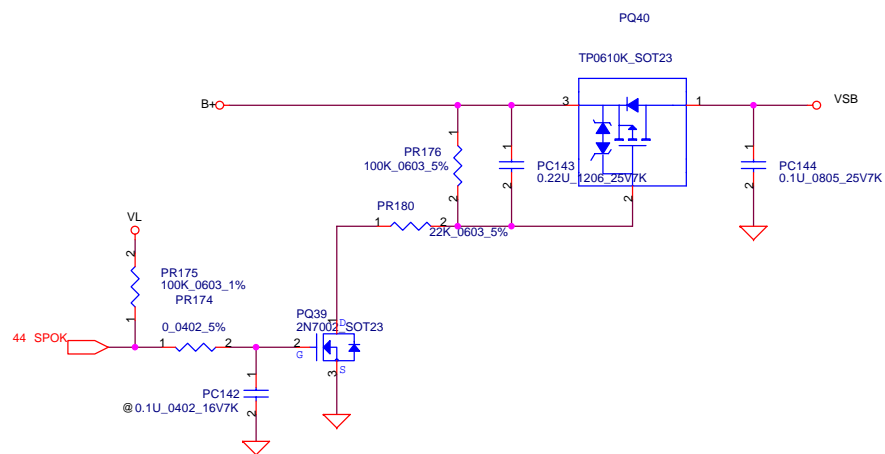
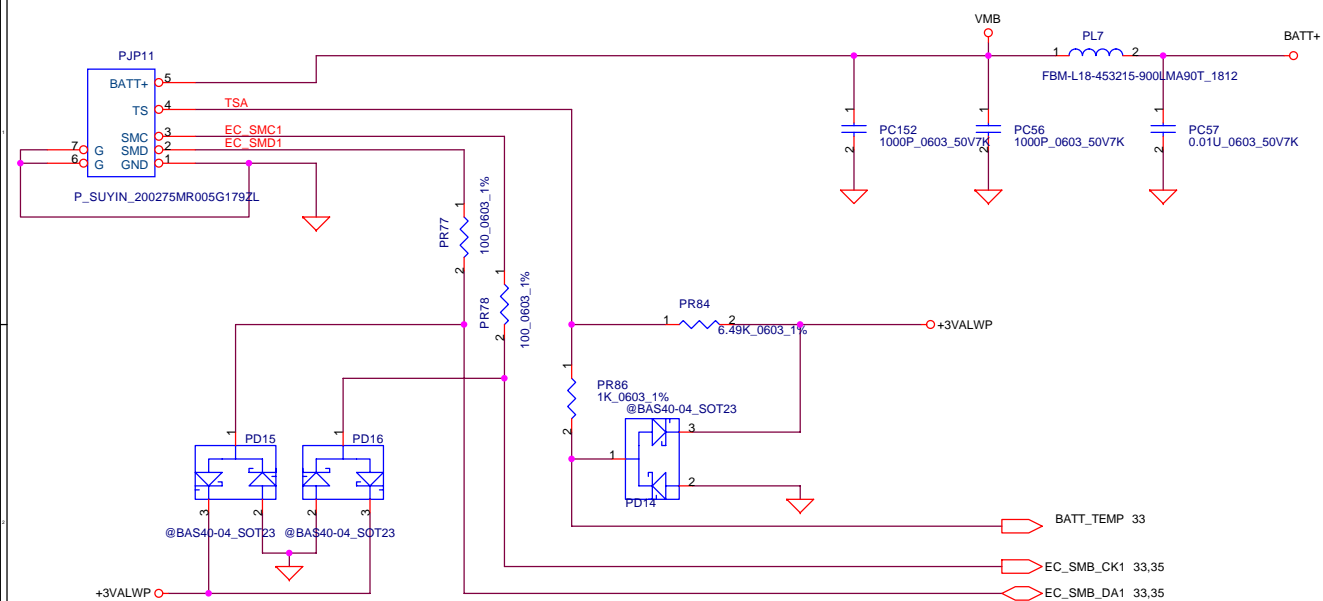


+3.3V Ipeak = 6.66A ~ 10A

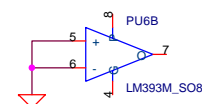
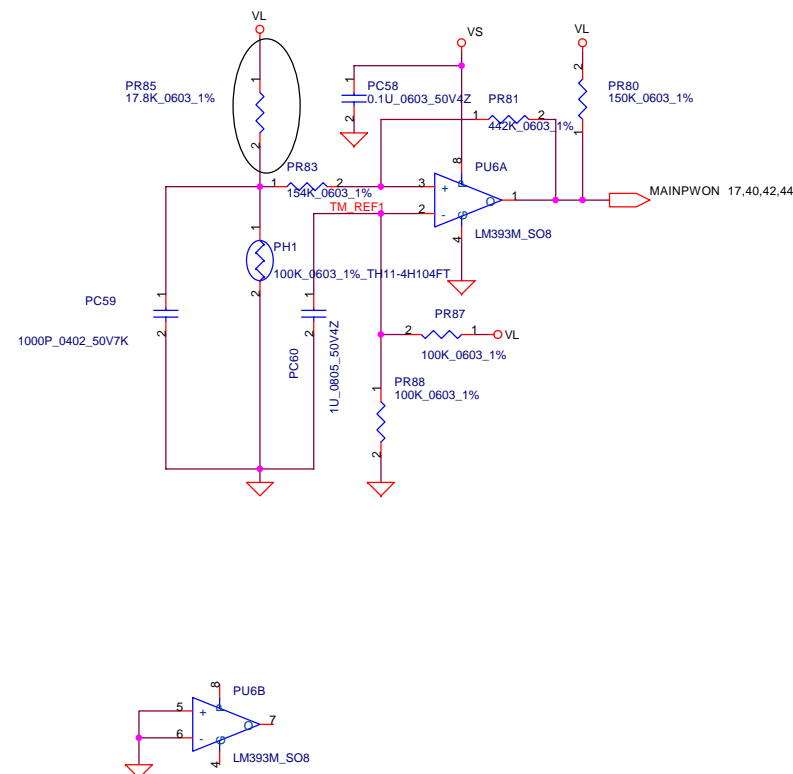
+5V Ipeak = 6.66A ~ 10A

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Compal Electronics, Inc.			
Title		5V/3.3V/12V	
Size	Document Number		Rev
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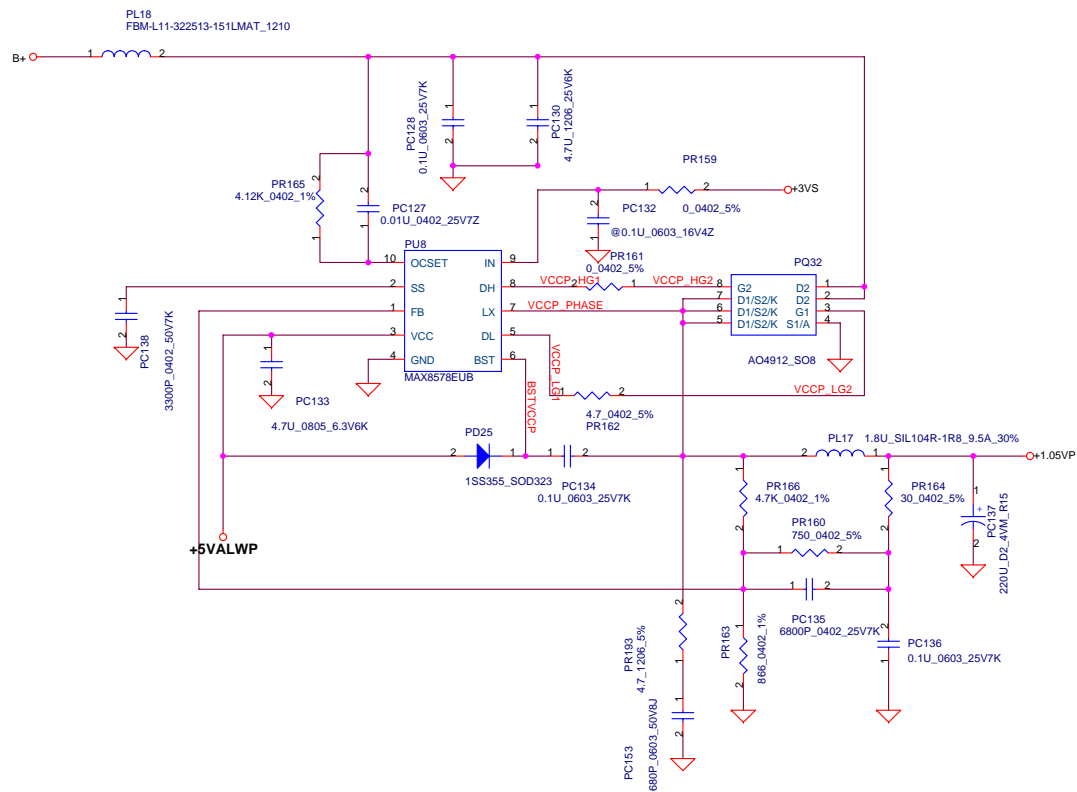
PH1 under CPU botten side :
CPU thermal protection at 80 degree C
Recovery at 44(45) degree C



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Compal Electronics, Inc.

Title		BATTERY CONN / OTP/1.2V	
Size	Document Number	Rev	
B	EFL50 LA-2761	0.1	
Date:	Wednesday, April 20, 2005	Sheet	45 of 51

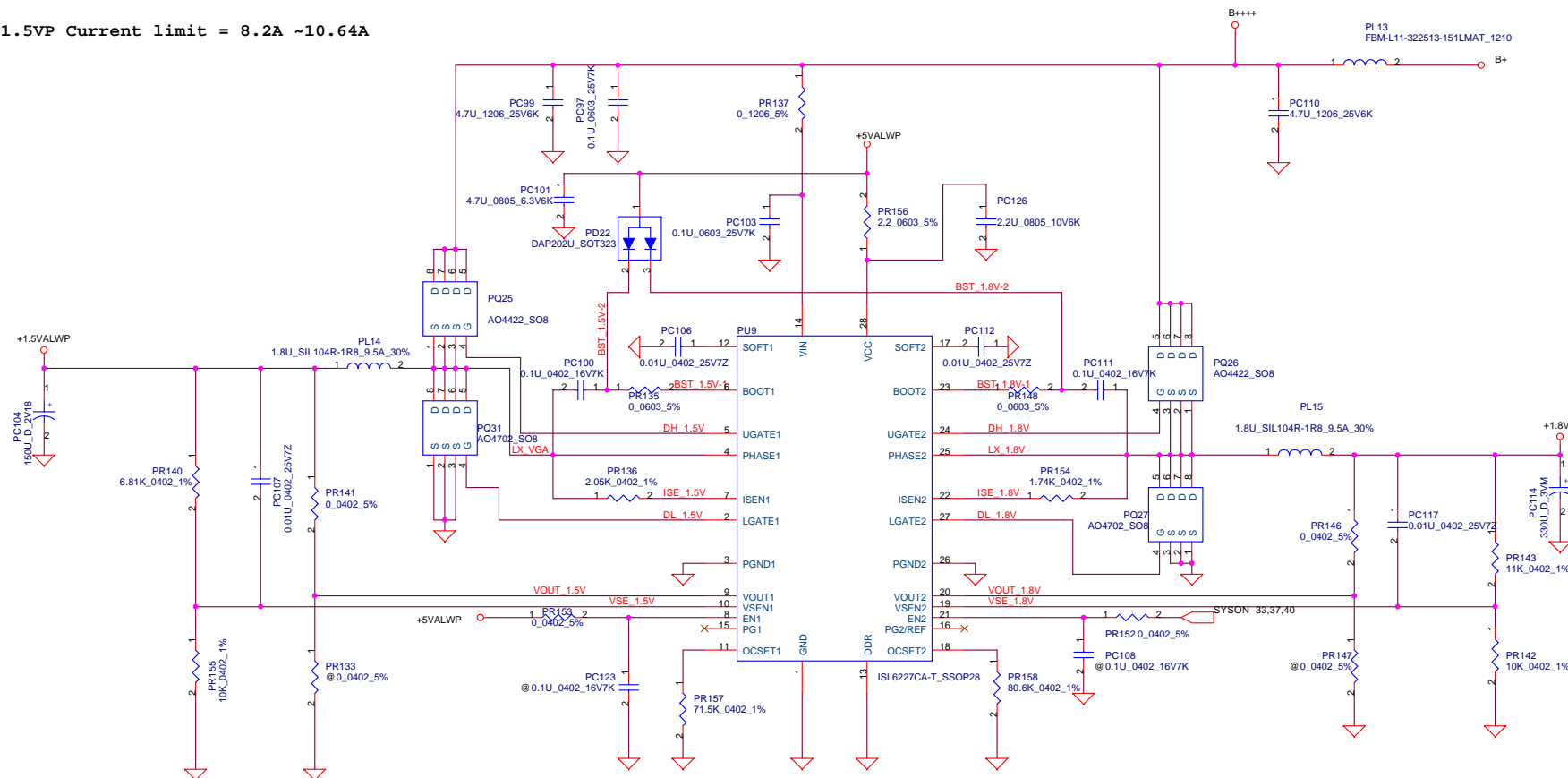


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Compal Electronics, Inc.			
Title			
1.5V & 1.8V			
Size	Document Number		Rev
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+1.8VP O.C.P. =8.1A ~ 10.36A

+1.5VP Current limit = 8.2A ~10.64A



915PM(DISCRETE)	PR143=11K 1.8VP=1.867V
915GM (UMA)	PR143=10.5K 1.8VP=1.8V

Compal Electronics, Inc.

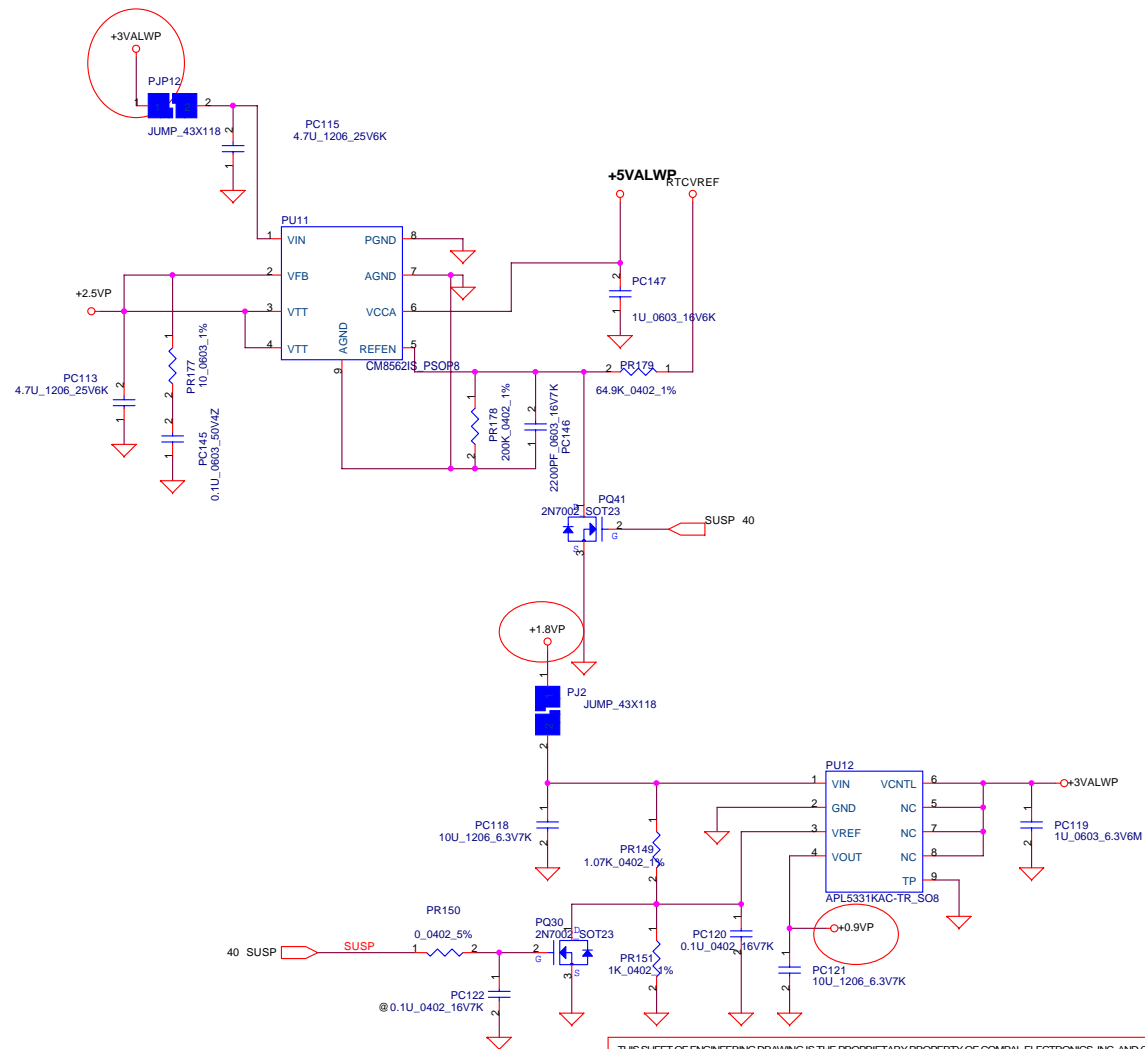
Title
1.5VP

Size
B

Document Number
EFL50 LA-2761

Rev
U.1

1



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Compal Electronics, Inc.			
Title			
1.8VP/0.9VP/2.5VP			
Size	Document Number		Rev
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Version change list (P.I.R. List)

Page 1 of 1 for HW

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	VER	Phase
1	Add C663, C664, C665	For +2.5V (VCC_SYNC)	0.2	9			
2	Add C658, C659, C660, C661, C662	For EMI Solution	0.2	11			
3	Change L43, L44, L45, L46 Net	Improve Audio	0.2	31			
4							
5							
6							
7							
8							
9							

Security Classification		Compal Secret Data		Title	
Issued Date	2005/03/08	Deciphered Date	2006/03/08	PIR (HW)	
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				Date: Wednesday, April 20, 2005	Rev 0.2
				Sheet 50 of 51	

Version change list (P.I.R. List)

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	VER	Phase
1		Modify PU8 IC		47	1. change PU8 from MAX8576 to MAX8578 2.delete PC132		DVT
2		Modify 1.05VP voltage level		47	1. change PR166 from 7.15k to 4.7k , PC135 from 6800P to 0.047u 0603		DVT
3		change precharge detect point		42	1. change PR20 from 412k to 330k		DVT
4		Modify disable precharge respond time		42	1. change PR167/PR168/PR169 from 470k to 100k		DVT
5		PD16 PIN2 AND PIN3 are wrong		45	1. modify PD16 schematic,and delete PD16(only reserve)		DVT
6		for decrease BATT connector EMI		45	1. add PC151/PC152/PC153 :680PF		DVT
7		for decrease CPU CORE switching ring effect and add EMI solution : snubber		46	1. change PR94/PR116 from 0 to 2.2 add PR189//PR188 4.7 1206 ,add PC150/PC149 680P		DVT
8		for decreaseVCCP ripple		47	1. change PC137 from 150u 35m to 220u 15m		DVT2
9		add EMI solution		45 46 43	1. add PC152 1. add PR190/191:2.2 1. add PR192:2.2		DVT2
10		add EMI solution		47	1. add PL18 FBM L11		PVT
11		add EMI solution in 1.05VP power regulator		47	1. add PC153=680PF ,PR193=4.7		PVT
8							
9							